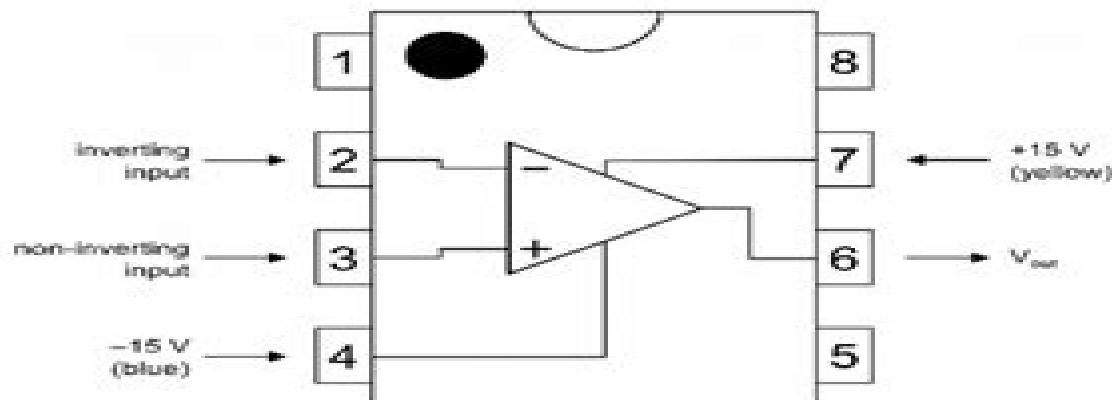


Type 741 Operational Amplifier



Above is the pin diagram of an operational amplifier, commonly called an op-amp. You will study op-amps in detail in EENGN 231 but, for now, there are two important points to know about their operation:

1. There is no current into either input.
2. There is no voltage difference between the two inputs.

The op-amp is an integrated circuit contained in an 8-pin dual inline package (DIP), also commonly called a chip. Your proto-boards are designed so that you can place a chip across the gap between the five-pin rows and have all of the pins on the DIP electrically isolated from one another. Note that there are both a semi-circular depression and a shiny black dot at one end of the chip; these are used as reference points when you are trying to determine which pin is where. It is very important that you use the correct pins, as using the wrong pins can not only result in a circuit that does not function as designed; it can destroy the op-amp. Do not confuse the inverting and non-inverting signal inputs (Pins 2 and 3) with the positive and negative supply voltages (Pins 7 and 4), and do not get the supply voltages mixed up. Once you have connected the two supply voltages, you should not have to touch them again until you are done with that op-amp.

Op Amp 741 Lab Manual

Dan Budny

Op Amp 741 Lab Manual:

ELECTRONICS LAB MANUAL (VOLUME 2) NAVAS, K. A., 2018-10-01 This book is evolved from the experience of the author who taught all lab courses in his three decades of teaching in various universities in India. The objective of this lab manual is to provide information to undergraduate students to practice experiments in electronics laboratories. This book covers 118 experiments for linear analog integrated circuits lab, communication engineering lab, power electronics lab, microwave lab and optical communication lab. The experiments described in this book enable the students to learn Various analog integrated circuits and their functions, Analog and digital communication techniques, Power electronics circuits and their functions, Microwave equipment and components, Optical communication devices. This book is intended for the B Tech students of Electronics and Communication Engineering, Electrical and Electronics Engineering, Biomedical Electronics, Instrumentation and Control, Computer Science and Applied Electronics. It is designed not only for engineering students but can also be used by BSc, MSc Physics and Diploma students.

KEY FEATURES: Contains aim, components and equipment required, theory, circuit diagram, pin outs of active devices, design tables, graphs, alternate circuits and troubleshooting techniques for each experiment. Includes viva voce and examination questions with their answers. Provides exposure on various devices.

TARGET AUDIENCE: B Tech Electronics and Communication Engineering, Electrical and Electronics Engineering, Biomedical Electronics, Instrumentation and Control, Computer Science and Applied Electronics, BSc, MSc Physics, Diploma Engineering.

Laboratory Manual for Electronic Devices and Circuits David A. Bell, 2001 This lab manual accompanies Electronic Devices and Circuits 4 e

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Analog Electronic Circuits and Systems Michael A. Soderstrand,Gary E. Ford,2003-12-04 *Popular Electronics* ,1979 **EDN.** ,1977

Proceedings American Society for Engineering Education. Conference,1994 *Laboratory Manual for Introductory Electronics Experiments* L. K. Maheshwari,M. M. S. Anand,1979 **Engineering Education for the 21st Century** Dan Budny,1995 **Radio-electronics** ,1978 *73 Amateur Radio's Technical Journal* ,1974

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