

# **Parallel Supercomputing in MIMD Architectures**

**R. Michael Hord**

# Parallel Supercomputing In Mimd Architectures

**Jeremy Alan Salinger**



## **Parallel Supercomputing In Mimd Architectures:**

Parallel Supercomputing in MIMD Architectures R. Michael Hord, 2018-02-01 Parallel Supercomputing in MIMD Architectures is devoted to supercomputing on a wide variety of Multiple Instruction Multiple Data MIMD class parallel machines This book describes architectural concepts commercial and research hardware implementations major programming concepts algorithmic methods representative applications and benefits and drawbacks Commercial machines described include Connection Machine 5 NCUBE Butterfly Meiko Intel iPSC iPSC 2 and iWarp DSP3 Multimax Sequent and Teradata Research machines covered include the J Machine PAX Concert and ASP Operating systems languages translating sequential programs to parallel and semiautomatic parallelizing are aspects of MIMD software addressed in Parallel Supercomputing in MIMD Architectures MIMD issues such as scalability partitioning processor utilization and heterogenous networks are discussed as well This book is packed with important information and richly illustrated with diagrams and tables Parallel Supercomputing in MIMD Architectures is an essential reference for computer professionals program managers applications system designers scientists engineers and students in the computer sciences **Parallel**

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Next the co sponsors Agfa Gevaert N V Control Data Belgium and the Belgian Ministry of Education Special thanks are due to Dr P E Van Camp and Drs H Nachtegale for the practical organization of this workshop I would also like to thank Mrs H Evans for typing the manuscripts and for preparing the author and subject index v Last but not least I express my gratitude to Mr

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Hockney,C.R Jesshope,1988-01-01 Since the publication of the first edition parallel computing technology has gained considerable momentum A large proportion of this has come from the improvement in VLSI techniques offering one to two orders of magnitude more devices than previously possible A second contributing factor in the fast development of the subject is commercialization The supercomputer is no longer restricted to a few well established research institutions and large companies A new computer breed combining the architectural advantages of the supercomputer with the advance of VLSI technology is now available at very attractive prices A pioneering device in this development is the transputer a VLSI processor specifically designed to operate in large concurrent systems *Parallel Computers 2 Architecture Programming and Algorithms* reflects the shift in emphasis of parallel computing and tracks the development of supercomputers in the years since the first edition was published It looks at large scale parallelism as found in transputer ensembles This extensively rewritten second edition includes major new sections on the transputer and the OCCAM language The book contains specific information on the various types of machines available details of computer architecture and technologies and descriptions of programming languages and algorithms Aimed at an advanced undergraduate and postgraduate level this handbook is also useful for research workers machine designers and programmers concerned with parallel computers In addition it will serve as a guide for potential parallel computer users especially in disciplines where large amounts of computer time are regularly used *Supercomputing '88: Supercomputer design: hardware & software* ,1988 Parallel Rendering Symposium ,1993

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