



Parallel Computation on Regular Arrays (Algorithms and Architectures for Advanced Scientific Computing)

Tchuenté, Maurice

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Parallel Computation On Regular Arrays Algorithms Architectures For Advanced Scientific Computing

Rose Arny



Parallel Computation On Regular Arrays Algorithms Architectures For Advanced Scientific Computing:

Parallel Computation on Regular Arrays Maurice Tchuente,1991 **Parallel Computation on Regular Arrays**

Maurice Tchuente,1991-07-18 Presents fundamental techniques for the design and analysis of parallel algorithms for regular arrays Most of the algorithms are derived from general methodologies with an emphasis on formulations which give a unified presentation *Parallelism in Matrix Computations* Efstratios Gallopoulos,Bernard Philippe,Ahmed H. Sameh,2015-07-25

This book is primarily intended as a research monograph that could also be used in graduate courses for the design of parallel algorithms in matrix computations It assumes general but not extensive knowledge of numerical linear algebra parallel architectures and parallel programming paradigms The book consists of four parts I Basics II Dense and Special Matrix Computations III Sparse Matrix Computations and IV Matrix functions and characteristics Part I deals with parallel programming paradigms and fundamental kernels including reordering schemes for sparse matrices Part II is devoted to dense matrix computations such as parallel algorithms for solving linear systems linear least squares the symmetric algebraic eigenvalue problem and the singular value decomposition It also deals with the development of parallel algorithms for special linear systems such as banded Vandermonde Toeplitz and block Toeplitz systems Part III addresses sparse matrix computations a the development of parallel iterative linear system solvers with emphasis on scalable preconditioners b parallel schemes for obtaining a few of the extreme eigenpairs or those contained in a given interval in the spectrum of a standard or generalized symmetric eigenvalue problem and c parallel methods for computing a few of the extreme singular triplets Part IV focuses on the development of parallel algorithms for matrix functions and special characteristics such as the matrix pseudospectrum and the determinant The book also reviews the theoretical and practical background necessary when designing these algorithms and includes an extensive bibliography that will be useful to researchers and students alike The book brings together many existing algorithms for the fundamental matrix computations that have a proven track record of efficient implementation in terms of data locality and data transfer on state of the art systems as well as several algorithms that are presented for the first time focusing on the opportunities for parallelism and algorithm robustness *Numerical*

Methods for Large Eigenvalue Problems Y. Saad,1992 **Mathematical Reviews** ,1994 Forthcoming Books Rose

Army,1992-10 Scientific and Technical Aerospace Reports ,1989 **Computer Publishers & Publications** ,1992

Subject Guide to Books in Print ,1993 *The British National Bibliography* Arthur James Wells,1995 **Advanced**

Computer Architecture D. P. Agrawal,1986 Computer architecture is expected to cover the gap between digital hardware and computer software This tutorial will emphasize the importance of such a close interaction and the impact of parallel distributed processing and VLSI technology will be clearly shown Other important issues include examination of tradeoffs in the design of supercomputers and potential advantages of unique architectural concepts Strategies for evaluating system performance will also be covered This tutorial is meant for system designers application engineers scientists researchers and

students Some background in computer organization architecture will be assumed

Journal of New Generation Computer Systems ,1990 **UCLA Computer Science Annual** ,1991 **High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures** K. J. Ray Liu,Kung Yao,1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures *American Book Publishing Record Cumulative 1993* R R Bowker Publishing,1994-03 Cited in BCL3 Sheehy and Walford Compiled from the 12 monthly issues of the ABPR this edition of the annual cumulation lists by Dewey sequence some 41 700 titles for books published or distributed in the US Entry information is derived from MARC II tapes and books submitted to R R Bowker and

Reviews in Numerical Analysis, 1980-86 ,1987 These five volumes bring together a wealth of bibliographic information in the area of numerical analysis Containing over 17 600 reviews of articles books and conference proceedings these volumes represent all the numerical analysis entries that appeared in Mathematical Reviews between 1980 and 1986 Author and key indexes appear at the end of volume 5 **Architectural Alternatives for Exploiting Parallelism** David J. Lilja,1991 Graduates advanced undergraduates and practicing engineers of computer architecture or system design may find interest in the survey of various architectures within which a computer can be induced to walk and chew gum at the same time The 37 reprinted journal articles and conference presentation **Energy Research Abstracts** ,1990 **Graduate Catalog** Iowa State University,1987 *Proceedings, ... International Parallel Processing Symposium & ... Symposium on Parallel and Distributed Processing* ,2000

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