

MICROELECTRONICS PACKAGING HANDBOOK

Technology Update



Editor

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Microelectronics Packaging Handbook Technology Drivers Part I Pt 1

R. R. Tummala, E.J. Rymaszewski



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Semiconductor Packaging Rao R. Tummala,Eugene J. Rymaszewski,Alan G. Klopfenstein,1997 *Handbook of Microelectronics Packaging and Interconnection Technologies* F. N. Sinnadurai,1985

Electronic Materials Handbook ,1989-11-01 Volume 1

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System on Package : Miniaturization of the Entire System Rao Tummala, 2008-04-15 System on Package SOP is an emerging microelectronic technology that places an entire system on a single chip size package Where systems used to be bulky boxes housing hundreds of components SOP saves

interconnection time and heat generation by keep a full system with computing communications and consumer functions all in a single chip Written by the Georgia Tech developers of the technology this book explains the basic parameters design functions and manufacturing issues showing electronic designers how this radical new packaging technology can be used to solve pressing electronics design challenges

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