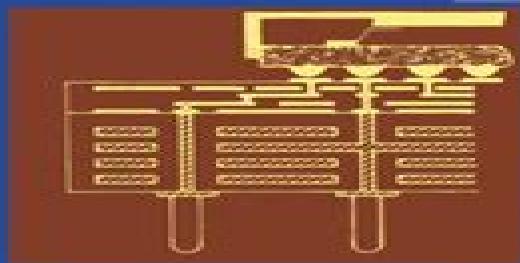


MICROELECTRONICS PACKAGING HANDBOOK

Semiconductor Packaging



PART II



Second Edition

Edited by

Rao R. Tummala
Eugene J. Rymaszewski
Alan G. Klopfenstein

Microelectronics Packaging Handbook Part 2

Semiconductor Packaging Pt 1

**Rao Tummala, Eugene J.
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Landzberg, 2012-12-06. The world of microelectronics is filled with success stories. From the use of semi control techniques test diagnostics and failure analysis. It discusses methods for modeling conductors for powerful desktop computers to their use in maintaining optimum engine performance and reducing defects and for preventing performance in modern automobiles. They have facts in the first place. The approach described clearly improved our daily lives. The broad while geared to the microelectronics world has usability of the technology is enabled how applicability to any manufacturing process of similar complexity. The authors comprise some ever only by the progress made in reducing their cost and improving their reliability. One of the best scientific minds in the world and defect reduction receives a significant focus in our area practitioners of the art. The information modern manufacturing world and high quality captured here is world class. I know you will diagnostics is the key step in that process find the material to be an excellent reference in of product failures enables step function Analysis your application improvements in yield and reliability which works to reduce cost and open up new opportunities. Dr. Paul R. Low applications and technologies IBM Vice President and This book describes the process of defect reduction of Technology Products General Manager in the microelectronics world.

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H.R. Kricheldorf, 2008-03-05. With contributions by numerous experts. **Microelectronics Packaging Handbook** Rao Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 1997-01-31. This thoroughly revised and updated three volume set continues to be the standard reference in the field providing the latest in microelectronics design methods modeling tools simulation techniques and manufacturing procedures. Unlike reference books that focus only on a few aspects of microelectronics packaging these outstanding volumes discuss state of the art packages that meet the power, cooling, protection and interconnection requirements of increasingly dense and fast microcircuitry. Providing an excellent balance of theory and practical applications this dynamic compilation features step by step examples and vital technical data simplifying each phase of package design and production. In addition the volumes contain over 2000 references, 900 figures and 250 tables. Part I Technology Drivers covers the driving force of microelectronics packaging electrical, thermal and reliability. It introduces the technology developer to aspects of manufacturing that must be considered during product development. Part II Semiconductor Packaging discusses the interconnection of the IC chip to the first level of packaging and all first level packages. Electrical test, sealing and encapsulation technologies are also covered in detail. Part III Subsystem Packaging explores board level packaging as well as connectors, cables and optical packaging.

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