

## MOSFET Cross Reference Guide

Industry Part Number	Recommended Fairchild Device	Package	Industry Part Number	Recommended Fairchild Device	Package
2N7000	2N7000	TO-92, N	IRF7203	FDS9435A	SO-8, P
2N7002	2N7002	SOT-23, N	IRF7204	NDS8434A	SO-8, P
BS170	BS170	TO-92, N	IRF7205	FDS9435A	SO-8, P
BS270	BS270	TO-92, N	IRF7206	FDS9435A	SO-8, P
BSS100	BSS100	TO-92, N	IRF7207	FDS8926A	SO-8, Dual N
BSS110	BSS110	TO-92, P	IRF7208	FDS9930A	SO-8, Dual N
BSS123	BSS123	SOT-23, N	IRF7209	FDS8934A	SO-8, Dual P
BSS138	BSS138	SOT-23, N	IRF7210	FDS9947A	SO-8, Dual P
BSS84	BSS84	SOT-23, P	IRF7211	FDS8926A	SO-8, Comp N/P
IRF1010E	NDP7061L	TO-220, N	IRF7212	FDS8938A	SO-8, Comp N/P
IRF1010N	NDP7060L	TO-220, N	IRF7213	FDS8926A	SO-8, Dual N
IRF1104	NDP7052L	TO-220, N	IRF7214	FDS8912A	SO-8, Dual N
IRF1310N	NDP710A	TO-220, N	IRF7215	FDS8934A	SO-8, Dual P
IRF1310S	ND8710A	TO-220, N	IRF7216	FDS8947A	SO-8, Dual P
IRF2807	NDP706A	TO-220, N	IRF7217	FDS8926A	SO-8, Comp N/P
IRF2807S	ND8408A	TO-220, N	IRF7218	FDS8958A	SO-8, Comp N/P
IRF3205	NDP7052L	TO-220, N	IRF7289	FDS8958A	SO-8, Comp N/P
IRF3710	NDP710A	TO-220, N	IRF7401	NDS8426A	SO-8, N
IRF5110	NDP410A	TO-220, N	IRF7403	FDS8612A	SO-8, N
IRF5200	NDP410A	TO-220, N	IRF7404	NDS8434A	SO-8, P
IRF5208N	NDP510A	TO-220, N	IRF7405	FDS9435A	SO-8, P
IRF5208S	ND8410A	TO-220, N	IRF7413	FDS8690A	SO-8, N
IRF530	NDP510A	TO-220, N	IRF7413A	FDS8690A	SO-8, N
IRF5308N	NDP510A	TO-220, N	IRF7416	NDS8435A	SO-8, P
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IRF540S	NDP610A	TO-220, N	IRF7504™	NDS8302P	Micro, Dual P
IRF7101	NDS9956A	SO-8, Dual N	IRF7506™	NDS8304P	Micro, Dual P
IRF7102	NDS9959	SO-8, Dual N	IRF7507™	NDS8320C	Micro, Comp N/P
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IRF7201	FDS8612A	SO-8, N	IRF7606	FDS8690A	SO-8, N
IRF7202	NDS8400A	SO-8, P	IRF7607	FDS8612A	SO-8, N

# Mosfet Reference Guide

**Marcel A. Müller**

## **Mosfet Reference Guide:**

**MOSFET Modeling & BSIM3 User's Guide** Yuhua Cheng,Chenming Hu,2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike     [Essential Circuits Reference Guide](#) John Markus,Charles D. Weston,1988 A guide to research this volume includes 925 studies of Chaucer written between 1900 and 1984 Each entry is listed once alphabetically under an appropriate topic heading or under the title of the work it treats most directly The annotations provide bibliographic information identify the primary focus of the item annotated and summarize its content See entry PR1868 These classic circuits were chosen from Markus Sourcebook of electronic circuits 1968 Electronics circuits manual 1971 and Guidebook of electronics circuits 1974 With circuit integration onto chips many older circuits have become obsolete This guide is a distillation of those circuits still in use today for which parts are still available Annotation copyrighted by Book News Inc Portland OR     **MOSFET Modeling & BSIM3 User's Guide** Yuhua Cheng,Chenming Hu,1999-09-30 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most

circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike

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**MOSFET Models for SPICE Simulation** William Liu, 2001-02-21 An expert guide to understanding and making optimum use of BSIM Used by more chip designers worldwide than any other comparable model the Berkeley Short Channel IGFET Model BSIM has over the past few years established itself as the de facto standard MOSFET SPICE model for circuit simulation and CMOS technology development Yet until now there have been no independent expert guides or tutorials to supplement the various BSIM manuals currently available Written by a noted expert in the field this book fills that gap in the literature by providing a comprehensive guide to understanding and making optimal use of BSIM3 and BSIM4 Drawing upon his extensive experience designing with BSIM William Liu provides a brief history of the model discusses the various advantages of BSIM over other models and explores the reasons why BSIM3 has been adopted by the majority of circuit manufacturers He then provides engineers with the detailed practical information and guidance they need to master all of BSIM's features He Summarizes key BSIM3 components Represents the BSIM3 model with equivalent circuits for various operating conditions Provides a comprehensive glossary of modeling terminology Lists alphabetically BSIM3 parameters along with their meanings and relevant equations Explores BSIM3's flaws and provides improvement suggestions Describes all of BSIM4's improvements and new features Provides useful SPICE files which are available online at the Wiley ftp site

**1993 Applications Reference Manual** Analog Devices, inc, 1993 **Mosfet Modeling and Bsim3 User's Guide**  
Yuhua Cheng, Chenming Hu, 2014-01-15 **SPICE Version 2G.1 User's Guide** Andrei Vladimirescu, 1980 **IEEE**

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Kurt Hoffmann,2004-04-16 The development of large scale integrated systems on a chip has had a dramatic effect on circuit design methodology Escalating requirements for low power high chip density circuits and systems have resulted in increasingly complex Bipolar CMOS and BICMOS technologies In order to design cost effective and reliable systems a thorough understanding of the relationship between the individual components and their impact on the performance of the integrated system is required Combining coverage of semiconductor physics digital VLSI design and analog integrated circuits in one volume this book will appeal to practising engineers circuit designers and physicists working in IC production product engineering quality management and quality testing Postgraduate students in microelectronics electrical and computer engineering will also find this book a useful reference Midwest

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