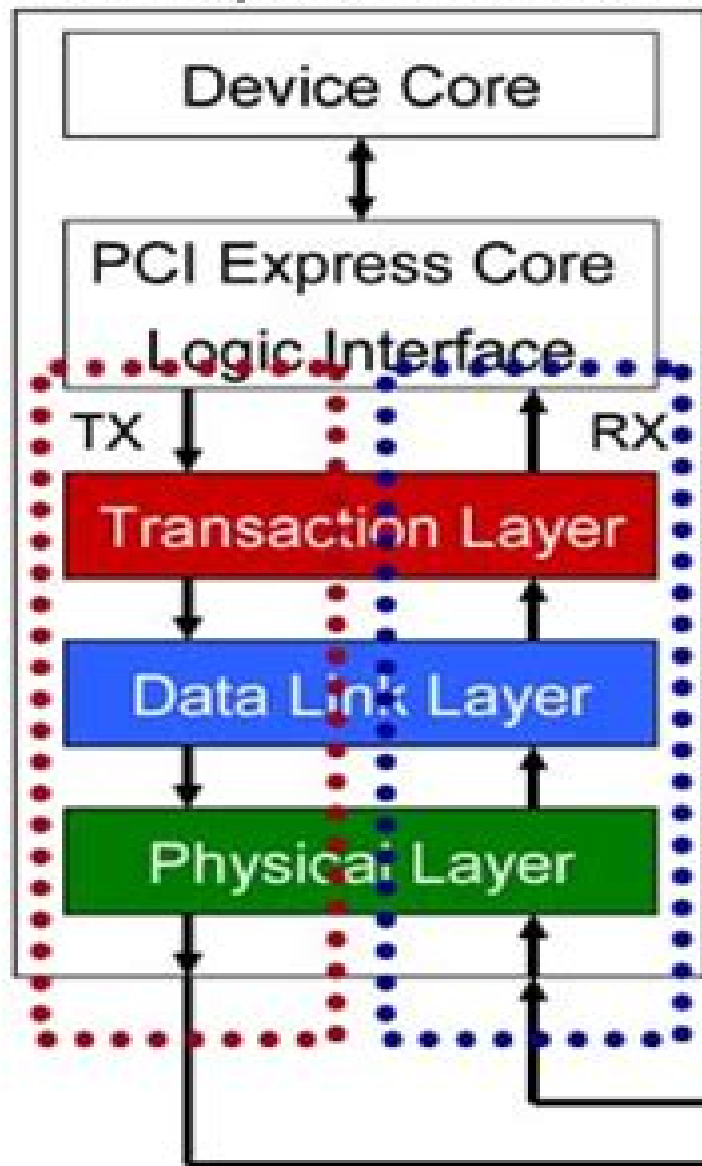
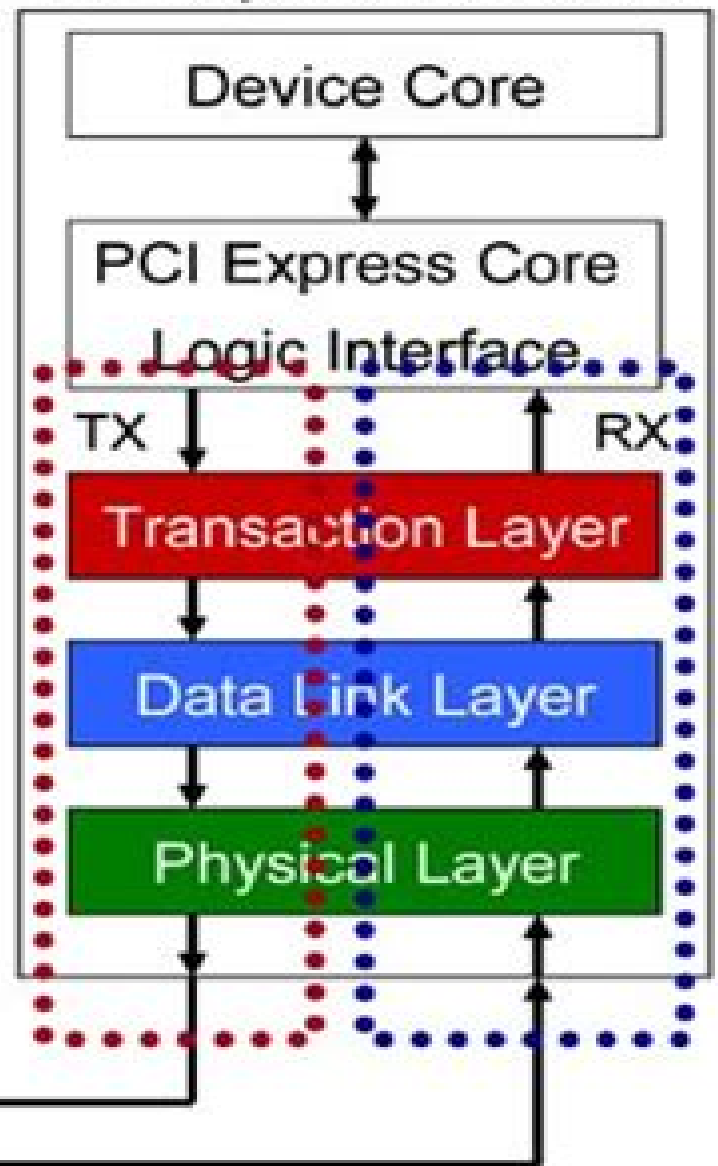


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# Pci Express System Architecture

**Roman Wölfel**

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*Introduction to PCI Express* Adam H. Wilen, Justin P. Schade, Ron Thornburg, 2003 Offering an overview this guide details how 3GIO allows designers to overcome the practical performance limits of existing multidrop parallel bus technology and explains how to increase performance and new capabilities for a broad range of computing and communications platforms

*Implementation of a PCI-Express Based System Architecture for an Adaptive Computer System* Raad Bahmani, 2014

*Real-time Embedded Components and Systems* Sam Siewert, 2007

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*Proceedings of the 12th Workshop on Electronics for LHC and Future Experiments, Valencia, Spain, 25-29 September 2006*, 2007

**CERN.**, 2007

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**Computer Systems Series**, 1997

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