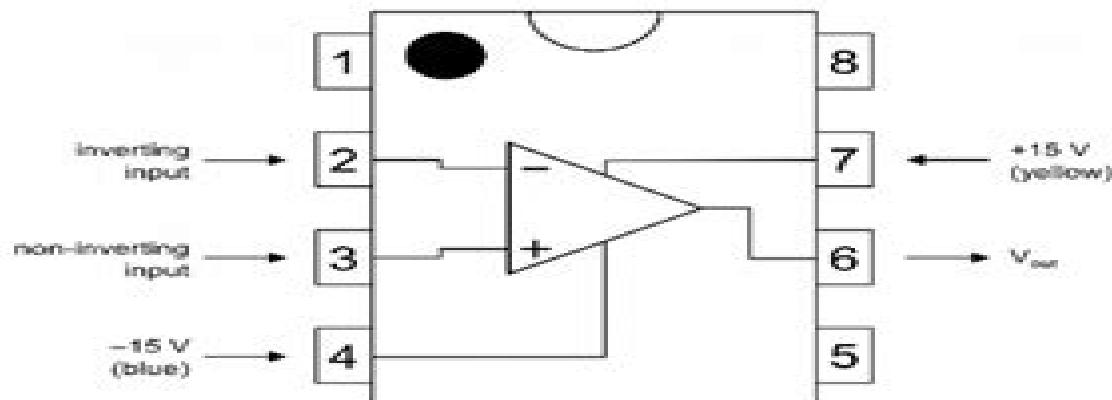


Type 741 Operational Amplifier



Above is the pin diagram of an operational amplifier, commonly called an op-amp. You will study op-amps in detail in EENGN 231 but, for now, there are two important points to know about their operation:

1. There is no current into either input.
2. There is no voltage difference between the two inputs.

The op-amp is an integrated circuit contained in an 8-pin dual inline package (DIP), also commonly called a chip. Your proto-boards are designed so that you can place a chip across the gap between the five-pin rows and have all of the pins on the DIP electrically isolated from one another. Note that there are both a semi-circular depression and a shiny black dot at one end of the chip; these are used as reference points when you are trying to determine which pin is where. It is very important that you use the correct pins, as using the wrong pins can not only result in a circuit that does not function as designed; it can destroy the op-amp. Do not confuse the inverting and non-inverting signal inputs (Pins 2 and 3) with the positive and negative supply voltages (Pins 7 and 4), and do not get the supply voltages mixed up. Once you have connected the two supply voltages, you should not have to touch them again until you are done with that op-amp.

Op Amp 741 Lab Manual

Roger L. Tokheim

Op Amp 741 Lab Manual:

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web curriculum first year first semester course code course name metu credit theoretical h w laboratory h w ects credit phys105 general physics i 4 3 2 6 5 chem107 general chemistry 4 3 2 6 0 courses given by eee dept full list of courses given bu our department is given on courses page

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web curriculum the eee ms program consists of 7 regular courses one seminar course and thesis research which must add up to at least 21 credits the 3 mandatory elective courses should be chosen from the 7 suggested elective courses the 3 mandatory elective courses are taken during the first two semesters

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