

Code No: 07A10401

Set No. 1

I.B.Tech Regular Examinations, May/Jun 2008
BASIC ELECTRONIC DEVICES AND CIRCUITS
(Electrical & Electronic Engineering)

Time: 3 hours

Max Marks: 80

Answer any FIVE Questions
All Questions carry equal marks

1. (a) Derive the expressions for acceleration, Velocity and displacement of a charged particle placed in an electric field E .
(b) Two parallel plates of a capacitor are separated by 4 cms. An electron is at rest initially at the bottom plate. Voltage is applied between the plates, which increases linearly from 0V to 8V in 0.1 m.sec. If the top plate is +ve, determine:
 - i. The speed of electron in 40 m sec
 - ii. The distance traversed by the electron in 40 m sec. [8+8]
2. (a) What are the various applications of p-n junction diode? Explain them.
(b) What are the specifications of p-n junction diode? Explain how reverse saturation current varies with temperature both Si and Ge diodes.
(c) Explain about the characteristics of zener diode. [5+6+5]
3. (a) A 15-0-15 Volts (rms) ideal transformer is used with a full wave rectifier circuit with diodes having forward drop of 1 volt. The load is a resistance of 100 ohm and a capacitor of 10,000 F is used as filter across the load resistance. Calculate the dc load current and voltage.
(b) Explain the working of the half wave rectifier circuit with neat sketch of waveforms at various points in the circuit. [8+8]
4. (a) A transistor operating in CB configuration has $IC = 2.98 \text{ mA}$, $IE = 3.00 \text{ mA}$ and $ICO = 0.01 \text{ mA}$. What current will flow in the collector circuit of this transistor when connected in CE configuration with a base current of 30 A.
(b) The reverse saturation current in a transistor is 8 A. If the transistor common base current gain is 0.979, calculate the collector and emitter current for 40 A base current. [8+8]
5. (a) Compare the advantages and disadvantages of biasing schemes.
(b) Calculate the quiescent current and voltage of collector to base bias arrangement using the following data:
 $V_{cc} = 10V$, $R_b = 100K$, $R_c = 2K$, -50 and also specify a value of R_b so that $V_{ce} = 7V$. [8+8]
6. (a) A transistor used in CB circuit has the following set of h parameters, $h_{ib} = 20$, $h_{ie} = 0.98$, $h_{re} = 3 \times 10^{-4}$, $h_{oe} = 0.5 \times 10^{-6}$. Find the values of R_b , R_c , A_v and A_{vdc} [8+8]

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