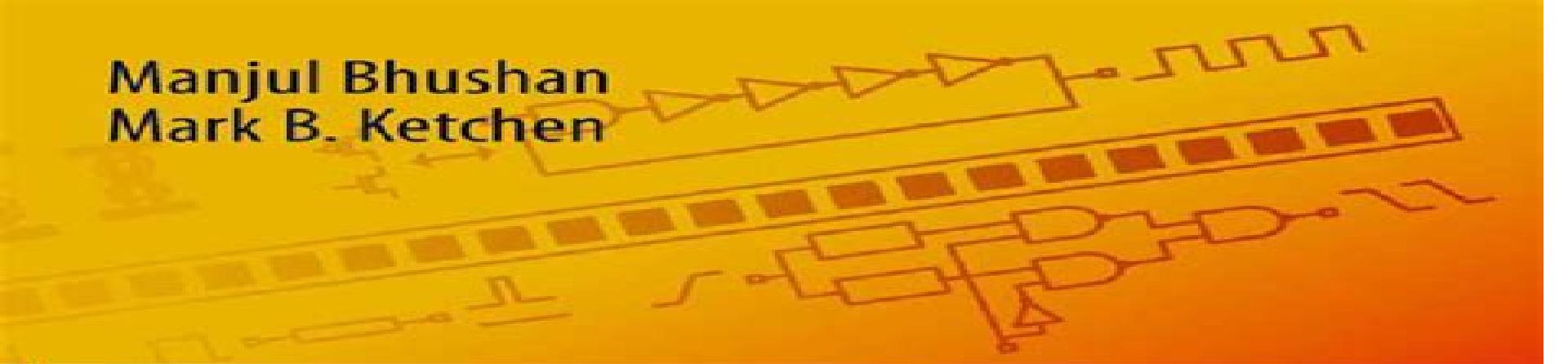


Manjul Bhushan  
Mark B. Ketchen



# Microelectronic Test Structures for CMOS Technology

# Microelectronic Test Structures For Cmos Technology

**SA Adler**

## Microelectronic Test Structures For Cmos Technology:

**Microelectronic Test Structures for CMOS Technology** Manjul Bhushan,Mark B. Ketchen,2011-08-26

Microelectronic Test Structures for CMOS Technology and Products addresses the basic concepts of the design of test structures for incorporation within test vehicles scribe lines and CMOS products The role of test structures in the development and monitoring of CMOS technologies and products has become ever more important with the increased cost and complexity of development and manufacturing In this timely volume IBM scientists Manjul Bhushan and Mark Ketchen emphasize high speed characterization techniques for digital CMOS circuit applications and bridging between circuit performance and characteristics of MOSFETs and other circuit elements Detailed examples are presented throughout many of which are equally applicable to other microelectronic technologies as well The authors overarching goal is to provide students and technology practitioners alike a practical guide to the disciplined design and use of test structures that give unambiguous information on the parametrics and performance of digital CMOS technology CMOS Test and Evaluation Manjul Bhushan,Mark B. Ketchen,2014-12-03 CMOS Test and Evaluation A Physical Perspective is a single source for an integrated view of test and data analysis methodology for CMOS products covering circuit sensitivities to MOSFET characteristics impact of silicon technology process variability applications of embedded test structures and sensors product yield and reliability over the lifetime of the product This book also covers statistical data analysis and visualization techniques test equipment and CMOS product specifications and examines product behavior over its full voltage temperature and frequency range *Proceedings of the ... IEEE International Conference on Microelectronic Test Structures* ,1988

**Latchup** Steven H. Voldman,2008-04-15 Interest in latchup is being renewed with the evolution of complimentary metal oxide semiconductor CMOS technology metal oxide semiconductor field effect transistor MOSFET scaling and high level system on chip SOC integration Clear methodologies that grant protection from latchup with insight into the physics technology and circuit issues involved are in increasing demand This book describes CMOS and BiCMOS semiconductor technology and their sensitivity to present day latchup phenomena from basic over voltage and over current conditions single event latchup SEL and cable discharge events CDE to latchup domino phenomena It contains chapters focusing on bipolar physics latchup theory latchup and guard ring characterization structures characterization testing product level test systems product level testing and experimental results Discussions on state of the art semiconductor processes design layout and circuit level and system level latchup solutions are also included as well as latchup semiconductor process solutions for both CMOS to BiCMOS such as shallow trench deep trench retrograde wells connecting implants sub collectors heavily doped buried layers and buried grids from single to triple well CMOS practical latchup design methods automated and bench level latchup testing methods and techniques latchup theory of logarithm resistance space generalized alpha a space beta b space new latchup design methods connecting the theoretical to the practical analysis and examples of latchup computer aided

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*Microelectronic Device Technology*, 1999      *1997 IEEE International Conference on Microelectronic Test Structures Proceedings* IEEE Electron Devices Society, 1997

**Analog-to-Digital Conversion** Marcel J.M.

Pelgrom, 2022-03-15 This textbook is appropriate for use in graduate level curricula in analog to digital conversion as well as for practicing engineers in need of a state of the art reference on data converters It discusses various analog to digital conversion principles including sampling quantization reference generation Nyquist architectures and sigma delta modulation This book presents an overview of the state of the art in this field and focuses on issues of optimizing accuracy and speed while reducing the power level This new fourth edition emphasizes novel calibration concepts the specific requirements of systems the consequences of advanced technology and the need for a more statistical approach to accuracy Pedagogical enhancements to this edition include additional new exercises solved examples to introduce all key new concepts and warnings remarks and hints from a practitioner's perspective wherever appropriate Considerable background information and practical tips from designing a PCB to lay out aspects to trade offs on system level complement the discussion of basic principles making this book a valuable reference for the experienced engineer

*ESD* Steven H. Voldman, 2011-04-04 Electrostatic discharge ESD continues to impact semiconductor components and systems as

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,1988 **Compact Modeling** Gennady Gildenblat,2010-06-22 Most of the recent texts on compact modeling are limited to a particular class of semiconductor devices and do not provide comprehensive coverage of the field Having a single comprehensive reference for the compact models of most commonly used semiconductor devices both active and passive represents a significant advantage for the reader Indeed several kinds of semiconductor devices are routinely encountered in a single IC design or in a single modeling support group Compact Modeling includes mostly the material that after several years of IC design applications has been found both theoretically sound and practically significant Assigning the individual chapters to the groups responsible for the definitive work on the subject assures the highest possible degree of expertise on each of the covered models **IEICE Transactions on Electronics** ,2005 **ISTFA 2009** ,2009-01-01 This volume features the latest research and practical data from the premier event for the microelectronics failure analysis community The papers cover a wide range of testing and failure analysis topics of practical value to anyone working to detect understand and eliminate electronic device and system failures Case histories and review papers are included as well as guides to new and unique tools and methodologies applications and results

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PMOS characteristics Small signal modeling and circuit analysis Resistor and capacitor design Current sources sinks and mirrors Basic symmetrical folded cascode and Miller OTAs Opamps with source follower and common source output stages Fully differential OTAs and opamps     Electrical Overstress/Electrostatic Discharge Symposium Proceedings ,2004

**Micromachined Devices and Components** ,1997     *ICMTS 2002* ,2002     **Electrical & Electronics Abstracts**  
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