



# Meter Uma Racing Wire

**R Barnett**



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binary multiplier takes binary numbers as inputs and produces a result in binary before moving forward lets quickly recap binary multiplication first  $0 \times 0 = 0$ ,  $0 \times 1 = 0$ ,  $1 \times 0 = 0$ ,  $1 \times 1 = 1$  so there is always a confusion in students

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web the verilog arithmetic operators all produce full precision results e.g. adding two 8 bit numbers produces a 9 bit result in many designs one chooses a word size many computers use 32 or 64 bits and all arithmetic results are truncated to that number of bits i.e. arithmetic is performed modulo 2word size

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web sequential multiplier assume the multiplicand a has n bits and the multiplier b has m bits if we only want to invest in a single n bit adder we can build a sequential circuit that processes a single partial product at a time and then cycle the circuit m times p b a s n nc n xn n n 1 s n 1 s 0 init p 0 load a and b repeat m times

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