

# *Microcontroller Solutions*

## *Chapter 5:-*

### *Section 5.1 and 5,2:-*

3. See on page 123 Figure 5-1, and on page 124 figure 5-2.
4. Register bank 1 , 2 , 3 share the space with stack because by default stack starts from 07H and after increment data is stored in 08H on the other hand Register bank 1 address starts from 08H as well see page 123 fig 5-1.
6. It copies the contents of the location 0F0H into the accumulator rather than the value 0F0H.
7. Same as question nos 6.

```
8. ORG 0000H
   MOV R0,#50H
   MOV R1,#40H
   MOV R3,#30H
   PUSH 00H
   PUSH 01H
   PUSH 03H
   POP 1DH POP
   1EH
   POP 1FH
   END
```

9. Registers R0 and R1.

```
10.ORG 0000H
    MOV A,#0FFH
    MOV R7,#32
    MOV R0,#50H
    NAME:MOV @R0,A
```

# Microcontroller Solution Manual

**Steven Barrett, Daniel Pack**



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[Solutions Manual for Using the MCS-51 Microcontroller](#) Han-Way Huang, 2000 This extensively detailed and comprehensive introduction to the Intel MCS 51 microcontroller covers both theoretical and design and implementation issues The text begins with the MCS 51 CPU architecture and programming model and then discusses the details of the MCS 51 instruction set and assembly programming techniques It goes on to cover the full spectrum of I O functions of the MCS 51 variants progressively developing topics from the simple to the complex the author first deals with the general concept behind each I O function then discusses the specifics of the MCS 51 Numerous design examples and exercises illustrate the ideas presented helping students to grasp key concepts and learn the applications An ideal text for the first course in microprocessors or microcontrollers Using the MCS 51 Microcontroller also includes extensive program and interfacing examples and is a helpful reference for practicing engineers Features DT Employs a pedagogically sound approach that first outlines basic issues and then discusses the specifics of the MCS 51 DT Provides complete coverage of I O functions including parallel I O ports timer functions serial communication ports A D converters and serial expansion ports DT Incorporates several lab projects into most chapters DT Suggests several evaluation boards and software tools for program development and testing offers a tutorial for using one of the evaluation boards and its software tools DT Supplemental CD includes an evaluation version of MCS 51 development tools so that readers can test their programs DT Emphasizes design analysis examples include memory design timing analysis Centronics interface timing analysis i8255 interfacing timing verification and LED and seven segment display electrical load analysis DT Includes extensive examples covering keypad scanning debouncing Centronics printer interface memory system design verification A D conversion D A conversion motor control RS 232 standard and more DT Solutions manual and transparencies available to adopters

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