+5

89s52f

31 $\overline{EA}/VP$

19 XTAL1

Cf1

Yf

GND

Cf2

18 XTAL2

+5

Cf3

+

SW

9 RESET

Rf1

GND

P32 12 P3.2(INT0)
P33 13 P3.3(INT1)
S2   14 P3.4(T0)
S3   15 P3.5(T1)

P10 1 P1.0
P11 2 P1.1
P12 3 P1.2
P13 4 P1.3
P14 5 P1.4
P15 6 P1.5
P16 7 P1.6
P17 8 P1.7

P0.0(AD0) 39 P00
P0.1(AD1) 38 P01
P0.2(AD2) 37 P02
P0.3(AD3) 36 P03
P0.4(AD4) 35 P04
P0.5(AD5) 34 P05
P0.6(AD6) 33 P06
P0.7(AD7) 32 P07

P2.0(A8)   21 P20
P2.1(A9)   22 P21
P2.2(A10) 23 P22
P2.3(A11) 24 P23
P2.4(A12) 25 P24
P2.5(A13) 26 P25
P2.6(A14) 27 P26
P2.7(A15) 28 P27

P3.7($\overline{RD}$) 17 S4
P3.6($\overline{WR}$) 16 P36
$\overline{PSEN}$ 29
ALE/$\overline{P}$ 30 ALE
P3.1(TXD) 11 P31
P3.0(RXD) 10 P30

89S52

Figure 2. Microcontroller minimum system

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