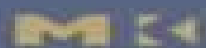# OPTIMIZING COMPILERS
for
MODERN ARCHITECTURES

Randy Allen & Ken Kennedy

Optimizing Compilers For Modern Architectures

Kathryn S. McKinley

Optimizing Compilers For Modern Architectures:

Optimizing Compilers for Modern Architectures: A Dependence-Based Approach Randy Allen,Ken Kennedy,2001-10 Modern computer architectures designed with high performance microprocessors offer tremendous potential gains in performance over previous designs Yet their very complexity makes it increasingly difficult to produce efficient code and to realize their full potential This landmark text from two leaders in the field focuses on the pivotal role that compilers can play in addressing this critical issue The basis for all the methods presented in this book is data dependence a fundamental compiler analysis tool for optimizing programs on high performance microprocessors and parallel architectures It enables compiler designers to write compilers that automatically transform simple sequential programs into forms that can exploit special features of these modern architectures The text provides a broad introduction to data dependence to the many transformation strategies it supports and to its applications to important optimization problems such as parallelization compiler memory hierarchy management and instruction scheduling The authors demonstrate the importance and wide applicability of dependence based compiler optimizations and give the compiler writer the basics needed to understand and implement them They also offer cookbook explanations for transforming applications by hand to computational scientists and engineers who are driven to obtain the best possible performance of their complex applications The approaches presented are based on research conducted over the past two decades emphasizing the strategies implemented in research prototypes at Rice University and in several associated commercial systems Randy Allen and Ken Kennedy have provided an indispensable resource for researchers practicing professionals and graduate students engaged in designing and optimizing compilers for modern computer architectures Offers a guide to the simple practical algorithms and approaches that are most effective in real world high performance microprocessor and parallel systems Demonstrates each transformation in worked examples Examines how two case study compilers implement the theories and practices described in each chapter Presents the most complete treatment of memory hierarchy issues of any compiler text Illustrates ordering relationships with dependence graphs throughout the book Applies the techniques to a variety of languages including Fortran 77 C hardware definition languages Fortran 90 and High Performance Fortran Provides extensive references to the most sophisticated algorithms known in research   The Compiler Design Handbook Y.N. Srikant,Priti Shankar,2018-10-03 Today s embedded devices and sensor networks are becoming more and more sophisticated requiring more efficient and highly flexible compilers Engineers are discovering that many of the compilers in use today are ill suited to meet the demands of more advanced computer architectures Updated to include the latest techniques The Compiler Design Handbook Second Edition offers a unique opportunity for designers and researchers to update their knowledge refine their skills and prepare for emerging innovations The completely revised handbook includes 14 new chapters addressing topics such as worst case execution time estimation garbage collection and energy aware compilation The editors take special care to consider the

growing proliferation of embedded devices as well as the need for efficient techniques to debug faulty code New contributors provide additional insight to chapters on register allocation software pipelining instruction scheduling and type systems Written by top researchers and designers from around the world The Compiler Design Handbook Second Edition gives designers the opportunity to incorporate and develop innovative techniques for optimization and code generation

Trustworthy Compilers Vladimir O. Safonov,2010-02-09 This unique guide book explains and teaches the concept of trustworthy compilers based on 50 years of worldwide experience in the area of compilers and on the author s own 30 years of expertise in development and teaching compilers It covers the key topics related to compiler development as well as compiling methods not thoroughly covered in other books The book also reveals many state of the art compiler development tools and personal experience of their use in research projects by the author and his team Software engineers of commercial companies and undergraduate graduate students will benefit from this guide **Itanium Architecture for Programmers** James S. Evans,Gregory L. Trimper,2003 Step by step guide to assembly language for the 64 bit Itanium processors with extensive examples Details of Explicitly Parallel Instruction Computing EPIC Instruction set addressing register stack engine predication I O procedure calls floating point operations and more Learn how to comprehend and optimize open source Intel and HP UX compiler output Understand the full power of 64 bit Itanium EPIC processorsItaniumreg Architecture for Programmersis a comprehensive introduction to the breakthrough capabilities of the new 64 bit Itanium architecture Using standard command line tools and extensive examples the authors illuminate the Itanium design within the broader context of contemporary computer architecture via a step by step investigation of Itanium assembly language Coverage includes The potential of Explicitly Parallel Instruction Computing EPIC Itanium instruction formats and addressing modes Innovations such as the register stack engine RSE and extensive predication Procedure calls and procedure calling mechanisms Floating point operations I O techniques from simple debugging to the use of files Optimization of output from open source Intel and HP UX compilers An essential resource for both computing professionals and students of architecture or assembly language Itanium Architecture for Programmers includes extensive printed and Web based references plus many numeric essay and programming exercises for each chapter **Introduction to Compiler Construction in a Java World** Bill Campbell,Swami Iyer,Bahar Akbal-Delibas,2012-11-21 Immersing students in Java and the JVM this text enables a deep understanding of the Java programming language and its implementation It focuses on design organization and testing helping students learn good software engineering skills and become better programmers By working with and extending a real functional compiler students develop a hands on appreciation of how compilers work how to write compilers and how the Java language behaves Fully documented Java code for the compiler is accessible on a supplementary website *Languages and Compilers for Parallel Computing* Barbara Chapman,José Moreira,2022-02-15 This book constitutes the thoroughly refereed post conference proceedings of the 33rd International Workshop on Languages and Compilers for Parallel

Computing LCPC 2020 held in Stony Brook NY USA in October 2020 Due to COVID 19 pandemic the conference was held virtually The 15 revised full papers were carefully reviewed and selected from 19 submissions The contributions were organized in topical sections named as follows Code and Data Transformations OpenMP and Fortran Domain Specific Compilation Machine Language and Quantum Computing Performance Analysis Code Generation   **Writing a C Compiler** Nora Sandler,2024-08-20 A fun hands on guide to writing your own compiler for a real world programming language Compilers are at the heart of everything programmers do yet even experienced developers find them intimidating For those eager to truly grasp how compilers work Writing a C Compiler dispels the mystery This book guides you through a fun and engaging project where you ll learn what it takes to compile a real world programming language to actual assembly code Writing a C Compiler will take you step by step through the process of building your own compiler for a significant subset of C no prior experience with compiler construction or assembly code needed Once you ve built a working compiler for the simplest C program you ll add new features chapter by chapter The algorithms in the book are all in pseudocode so you can implement your compiler in whatever language you like Along the way you ll explore key concepts like Lexing and parsing Learn how to write a lexer and recursive descent parser that transform C code into an abstract syntax tree Program analysis Discover how to analyze a program to understand its behavior and detect errors Code generation Learn how to translate C language constructs like arithmetic operations function calls and control flow statements into x64 assembly code Optimization techniques Improve performance with methods like constant folding dead store elimination and register allocation Compilers aren t terrifying beasts and with help from this hands on accessible guide you might even turn them into your friends for life   *Compilers: Principles and Practice* Parag H. Dave,Himanshu B. Dave, Compilers Principles and Practice explains the phases and implementation of compilers and interpreters using a large number of real life examples It includes examples from modern software practices such as Linux GNU Compiler Collection GCC and Perl This book has been class tested and tuned to the requirements of undergraduate computer engineering courses across universities in India **Construction of a Highly-optimizing Compiler for Variable Instruction Set Architecture** Jia-Ji Liu,2004 **Optimizing Partitioned Global Address Space Programs for Cluster Architectures** Wei-Yu Chen,2007 **Handbook of Parallel Computing and Statistics** Erricos John Kontoghiorghes,2005-12-21 Technological improvements continue to push back the frontier of processor speed in modern computers Unfortunately the computational intensity demanded by modern research problems grows even faster Parallel computing has emerged as the most successful bridge to this computational gap and many popular solutions have emerged based on its concepts   Languages and Compilers for Parallel Computing ,1991   **An Introduction to the Theory of Optimizing Compilers** Jonas Skeppstedt,2018-08-19 Updated with research up to 2018 and a new chapter on LLVM 6 0 1 the goal of the second edition of An Introduction to the Theory of Optimizing Compilers is to give the reader a solid understanding of modern optimizing

compilers The theory is focused on control flow graphs including algorithms for computing dominance dominance frontiers and control dependence static single assignment form SSA form including recent advances in translation from SSA form based on fast liveness checking and coalescing and loop transformations We also cover instruction scheduling and register allocation The optimization techniques we present include constant propagation with conditional branches partial redundancy elimination hash based and global value numbering operator strength reduction dead code elimination control flow graph simplification based on postdominators translation from SSA form based on coalescing unimodular loop transformations modulo scheduling and iterative register coalescing The book is to a high degree self contained and is intended to be suitable both for self study and university courses For completeness we provide a review of fundamentals including sections on sets and relations graphs number theory and some linear algebra In performance measurements on POWER using SPEC CPU2000 we compare gcc clang and the author s ISO validated C99 compiler Prerequisites The reader is assumed to have studied algorithms and data structures but no knowledge about compiler front ends is necessary About the author Dr Jonas Skeppstedt has done research on optimizing compilers and multicore computer architecture in Lund Chalmers and USC in Los Angeles his lmpcc compiler was rewarded ISO C certification in 2003 for C99 has taught optimizing compilers at Lund University for many years and has developed safety critical C code for the new European Rail Traffic Management System ERTMS and helped German lawyers as expert witness on the C programming language

Proceedings of the 2008 CGO ,2008 **Compiler Construction** ,2003 *Compiler Transformations for Automatic Generation of VHDL from C for Code Acceleration on Reconfigurable Devices* Ayse Betul Buyukkurt,2008 **ACM Transactions on Programming Languages and Systems** Association for Computing Machinery,2006 *20 Years of the ACM SIGPLAN Conference on Programming Language Design and Implementation* Kathryn S. McKinley,2004 Special volume of 50 selected papers with retrospectives from the original authors *IBM Journal of Research and Development* ,2006 SciDAC 2007 ,2007

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