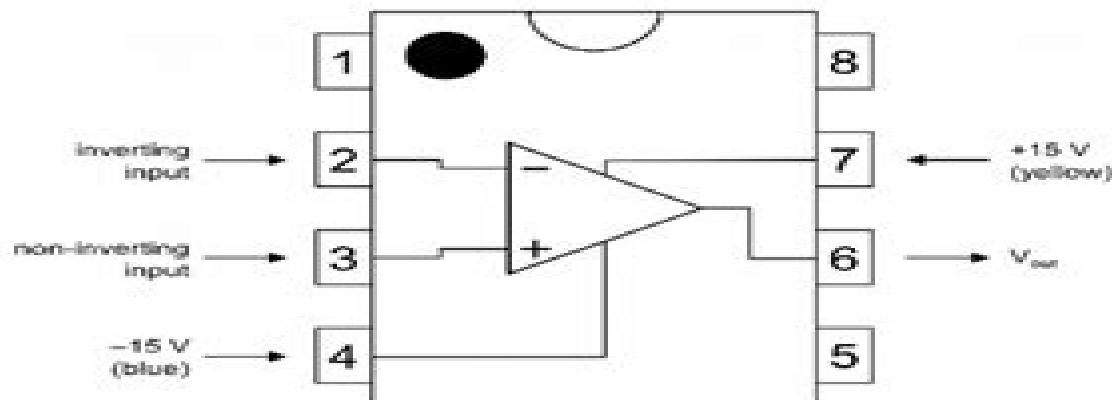


### Type 741 Operational Amplifier



Above is the pin diagram of an operational amplifier, commonly called an op-amp. You will study op-amps in detail in EENGN 231 but, for now, there are two important points to know about their operation:

1. There is no current into either input.
2. There is no voltage difference between the two inputs.

The op-amp is an integrated circuit contained in an 8-pin dual inline package (DIP), also commonly called a chip. Your proto-boards are designed so that you can place a chip across the gap between the five-pin rows and have all of the pins on the DIP electrically isolated from one another. Note that there are both a semi-circular depression and a shiny black dot at one end of the chip; these are used as reference points when you are trying to determine which pin is where. It is very important that you use the correct pins, as using the wrong pins can not only result in a circuit that does not function as designed; it can destroy the op-amp. Do not confuse the inverting and non-inverting signal inputs (Pins 2 and 3) with the positive and negative supply voltages (Pins 7 and 4), and do not get the supply voltages mixed up. Once you have connected the two supply voltages, you should not have to touch them again until you are done with that op-amp.

# Op Amp Manual

**L. K. Maheshwari,M. M. S. Anand**

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Satoshi Sakurai,Mohammed Ismail,2012-12-06 Low Voltage CMOS Operational Amplifiers Theory Design and Implementation discusses both single and two stage architectures Opamps with constant gm input stage are designed and their excellent performance over the rail to rail input common mode range is demonstrated The first set of CMOS constant gm input stages was introduced by a group from Technische Universiteit Delft and Universiteit Twente the Netherlands These earlier versions of circuits are discussed along with new circuits developed at the Ohio State University The design fabrication MOSIS Tiny Chips and characterization of the new circuits are now complete Basic analog integrated circuit design concepts should be understood in order to fully appreciate the work presented However the topics are presented in a logical order and the circuits are explained in great detail so that Low Voltage CMOS Operational Amplifiers can be read and enjoyed by those without much experience in analog circuit design It is an invaluable reference book and may be used as a text for advanced courses on the subject      *1993 Applications Reference Manual* Analog Devices, inc,1993      *Instructor's Manual for Operational Amplifiers and Linear ICS* David A. Bell,1997      *Audio IC Circuits Manual* R. M. Marston,2015-07-14 Audio IC Circuits Manual is a single volume practical user information and circuitry guide to the most popular and useful of audio and audio associated integrated circuits This book deals with ICs such as low frequency linear amplifiers dual pre amplifiers audio power amplifiers charged coupled device delay lines bar graph display drivers and power supply regulators This book is divided into seven chapters that focus on the application of these devices in circuits ranging from simple signal conditioners and filters to complex graphic equalizers stereo amplifier systems and echo reverb delay line systems Chapters 1 to 4 deal with pure audio subjects such as audio processing circuits audio pre amplifier circuits and audio power amplifier circuits Chapters 5 and 6 consider audio associated subjects of light emitting diode bar graph displays and CCD delay line circuits Chapter 7 deals with power supply circuits for use in audio systems This manual is intended primarily to design engineers technicians and electronic students      [Solution Manual Operational Amplifier Circuits](#) David E. Johnson,V. Jayakumar,1982

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