



# Verilog Manual Assertion

```
always_comb
if (sel == 1'b1)
    out = a;
else
    //pragma translate off
    if (sel == 1'b0)
        //pragma translate on
        out = b;
    //pragma translate off
    else // sel == 1'bX or 1'bZ
        begin
            out = 'x;
            $display(%m "assert:
                sel X or Z at time %d",
                $time);
        end
    //pragma translate on
```



```
always_comb
if (sel) out = a;
else out = b;
```



# Microprocessor And Verilog And Manual

**G Orfield**



## **Microprocessor And Verilog And Manual:**

## Unveiling the Power of Verbal Art: An Mental Sojourn through **Microprocessor And Verilog And Manual**

In a world inundated with monitors and the cacophony of quick conversation, the profound power and psychological resonance of verbal beauty often diminish in to obscurity, eclipsed by the regular onslaught of noise and distractions. However, situated within the musical pages of **Microprocessor And Verilog And Manual**, a captivating function of literary splendor that pulses with raw thoughts, lies an unforgettable journey waiting to be embarked upon. Composed by a virtuoso wordsmith, this mesmerizing opus books visitors on a psychological odyssey, gently exposing the latent possible and profound influence stuck within the elaborate web of language. Within the heart-wrenching expanse of the evocative examination, we shall embark upon an introspective exploration of the book is main subjects, dissect their fascinating writing fashion, and immerse ourselves in the indelible impression it leaves upon the depths of readers souls.

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