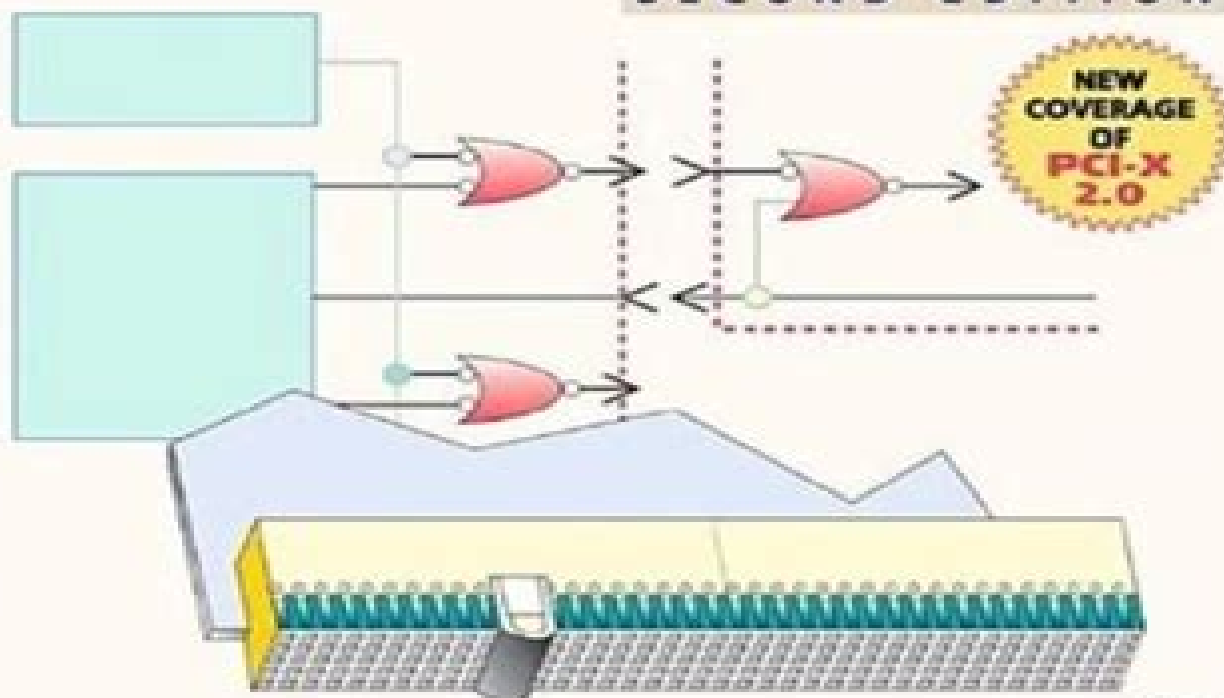




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**Telecommunications Demystified** Carl Nassar, 2001 Carl R. Nassar, Ph.D., is professor of telecommunications at Colorado State University and director of the Research in Advanced Wireless Communications (RAWCom) laboratory there. He also consults for telecommunications firms and publishes extensively in the wireless literature. Balances a solid theoretical treatment of subjects with practical applications and examples. Covers both digital and analogue telecommunications systems including digital modulation techniques. The CD accompanying the book includes MATLAB tutorials that permit readers to model various telecommunications systems and an electronic version of the book.

**Research in Attacks, Intrusions, and Defenses** Salvatore J. Stolfo, Angelos Stavrou, Charles V. Wright, 2013-10-23 This book constitutes the proceedings of the 16th International Symposium on Research in Attacks, Intrusions and Defenses (RAID 2013), held in Rodney Bay, St. Lucia, in October 2013. The volume contains 22 full papers that were carefully reviewed and selected from 95 submissions, as well as 10 poster papers selected from the 23 submissions. The papers address all current topics in computer security, ranging from hardware level security, server, web, mobile and cloud based security, malware analysis and web and network privacy.

**The British National Bibliography** Arthur James Wells, 2006

**PCI Express Technology** Mike Jackson, Ravi Budruk, MindShare, Don Anderson, 2011-03-01

**PCI System Architecture** Tom Shanley, Don Anderson, MindShare, Inc., 1995 PCI System Architecture describes revision 2.1 of the Peripheral Component Interconnect (PCI) bus specification, providing a clear, concise explanation of PCI's relationship to the rest of the system. This book has been updated and revised to include in-depth treatment of PCI-to-PCI bridges, the PCI BIOS, the 66MHz PCI bus, and more. PCI experts Tom Shanley and Don Anderson provide a comprehensive treatment of the bus. This book also examines the VLSI Technology VL82C59x chipset to illustrate an example PCI bus implementation.

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**PCI System Architecture**, 1999-09

*Using PCI-bus in Real-time Environments* Sebastian Schönberg, 2002

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