

## EXPERIMENT NO: 10

**AIM:** TO DESIGN & VERIFY OPERATION OF HALF ADDER & FULL ADDER.

**APPARATUS REQUIRED:** Power supply, IC's , Digital Trainer, Connecting leads .

**BRIEF THEORY:** We are familiar with ALU, which performs all arithmetic and logic operation but ALU doesn't perform/ process decimal no's. They process binary no's.

**Half Adder:** It is a logic circuit that adds two bits. It produces the O/P, sum & carry. The Boolean equation for sum & carry are

$$\text{SUM} = A + B$$

$$\text{CARRY} = A \cdot B$$

Therefore, sum produces 1 when A&B are different and carry is 1 when A&B are 1. Application of Half adder is limited.

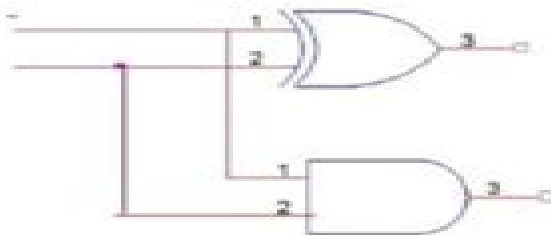
**Full Adder:** It is a logic circuit that can add three bits. It produces two O/P sum & carry. The Boolean Equation for sum & carry are

$$\text{SUM} = A + B + C$$

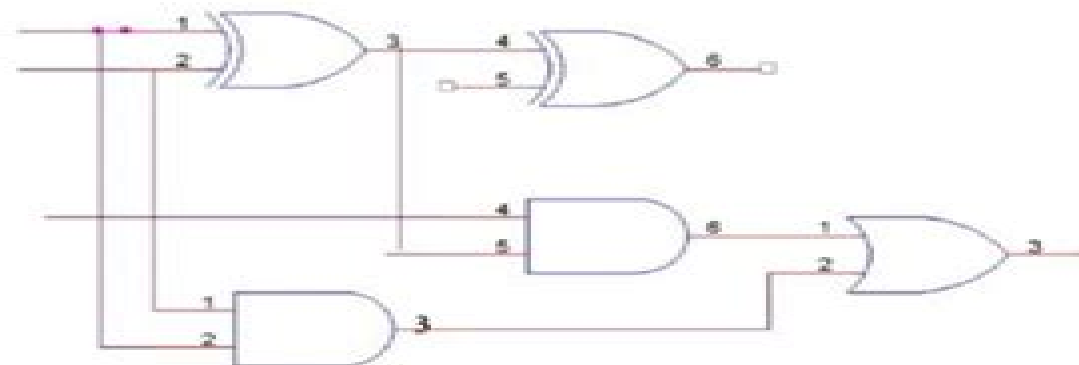
$$\text{CARRY} = A \cdot B + (A + B) \cdot C$$

Therefore, sum produces one when I/P is containing odd no's of one & carry is one when there are two or more one in I/P.

### CIRCUIT DAIGRAM



HALF ADDER



FULL ADDER

# Orcad Lab Manual For Adder

**Scott C. Dulebohn**



## **Orcad Lab Manual For Adder:**

Whitaker's Books in Print ,1998      *The Compact Scottish National Dictionary: A-M* William Grant,David D. Murison,1986

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### **Table of Contents Orcad Lab Manual For Adder**

1. Understanding the eBook Orcad Lab Manual For Adder
  - The Rise of Digital Reading Orcad Lab Manual For Adder
  - Advantages of eBooks Over Traditional Books
2. Identifying Orcad Lab Manual For Adder
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Orcad Lab Manual For Adder
  - User-Friendly Interface
4. Exploring eBook Recommendations from Orcad Lab Manual For Adder
  - Personalized Recommendations
  - Orcad Lab Manual For Adder User Reviews and Ratings
  - Orcad Lab Manual For Adder and Bestseller Lists

5. Accessing Orcad Lab Manual For Adder Free and Paid eBooks
  - Orcad Lab Manual For Adder Public Domain eBooks
  - Orcad Lab Manual For Adder eBook Subscription Services
  - Orcad Lab Manual For Adder Budget-Friendly Options
6. Navigating Orcad Lab Manual For Adder eBook Formats
  - ePub, PDF, MOBI, and More
  - Orcad Lab Manual For Adder Compatibility with Devices
  - Orcad Lab Manual For Adder Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Orcad Lab Manual For Adder
  - Highlighting and Note-Taking Orcad Lab Manual For Adder
  - Interactive Elements Orcad Lab Manual For Adder
8. Staying Engaged with Orcad Lab Manual For Adder
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Orcad Lab Manual For Adder
9. Balancing eBooks and Physical Books Orcad Lab Manual For Adder
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Orcad Lab Manual For Adder
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Orcad Lab Manual For Adder
  - Setting Reading Goals Orcad Lab Manual For Adder
  - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Orcad Lab Manual For Adder
  - Fact-Checking eBook Content of Orcad Lab Manual For Adder
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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