

MOSFET Cross Reference Guide

Industry Part Number	Recommended Fairchild Device	Package
2N7000	2N7000	TO-92, N
2N7002	2N7002	SOT-23, N
BS-170	BS-170	TO-92, N
BS270	BS270	TO-92, N
BSS100	BSS100	TO-92, N
BSS110	BSS110	TO-92, P
BSS123	BSS123	SOT-23, N
BSS138	BSS138	SOT-23, N
BSS84	BSS84	SOT-23, P
IRF1010E	NDP7061L	TO-220, N
IRF1010N	NDP7060L	TO-220, N
IRF1104	NDP7052L	TO-220, N
IRF1310N	NDP710A	TO-220, N
IRF1310S	NDB710A	TO-263, N
IRF2807	NDP706A	TO-220, N
IRF2807S	NDB408A	TO-263, N
IRF3205	NDP7052L	TO-220, N
IRF3710	NDP710A	TO-220, N
IRF510	NDP410A	TO-220, N
IRF520	NDP410A	TO-220, N
IRF520N	NDP510A	TO-220, N
IRF520S	NDB410A	TO-263, N
IRF530	NDP510A	TO-220, N
IRF530N	NDP510A	TO-220, N
IRF540	NDP610A	TO-220, N
IRF540N	NDP710A	TO-220, N
IRF540S	NDP610A	TO-220, N
IRF7101	NDS9956A	SO-8, Dual N
IRF7102	NDS9959	SO-8, Dual N
IRF7103	NDS9955	SO-8, Dual N
IRF7104	NDS9953A	SO-8, Dual P
IRF7105	NDS9952A	SO-8, Comp N/P
IRF7106	NDS9958	SO-8, Comp N/P
IRF7107	NDS9958	SO-8, Comp N/P
IRF7201	FDS6612A	SO-8, N
IRF7202	NDS9400A	SO-8, P

Industry Part Number	Recommended Fairchild Device	Package
IRF7203	FDS9435A	SO-8, P
IRF7204	NDS8434A	SO-8, P
IRF7205	FDS9435A	SO-8, P
IRF7206	FDS9435A	SO-8, P
IRF7301	FDS8926A	SO-8, Dual N
IRF7303	FDS8930A	SO-8, Dual N
IRF7304	FDS8934A	SO-8, Dual P
IRF7306	FDS8947A	SO-8, Dual P
IRF7307	FDS8928A	SO-8, Comp N/P
IRF7309	FDS8958A	SO-8, Comp N/P
IRF7311	FDS8926A	SO-8, Dual N
IRF7312	FDS8912A	SO-8, Dual N
IRF7314	FDS8934A	SO-8, Dual P
IRF7316	FDS8947A	SO-8, Dual P
IRF7317	FDS8928A	SO-8, Comp N/P
IRF7319	FDS8958A	SO-8, Comp N/P
IRF7389	FDS8958A	SO-8, Comp N/P
IRF7401	NDS8426A	SO-8, N
IRF7403	FDS6612A	SO-8, N
IRF7404	NDS8434A	SO-8, P
IRF7406	FDS9435A	SO-8, P
IRF7413	FDS6690A	SO-8, N
IRF7413A	FDS6690A	SO-8, N
IRF7416	NDS8435A	SO-8, P
IRF7501*	NDH8301N	Micro8, Dual N
IRF7503*	NDH8503N	Micro8, Dual N
IRF7504*	NDH8302P	Micro8, Dual P
IRF7506*	NDH8504P	Micro8, Dual P
IRF7507*	NDH8320C	Micro8, Comp N/P
IRF7509*	NDH8520C	Micro8, Comp N/P
IRF7601*	NDH831N	Micro8, N
IRF7603*	NDH8436	Micro8, N
IRF7604*	NDH830P	Micro8, P
IRF7606	NDH8447	Micro8, P
IRF7805	FDS6690A	SO-8, N
IRF7807	FDS6612A	SO-8, N

Note:

* SuperSOT-8 footprint not equal to TSSOP-8

Mosfet Reference Guide

Shasha Hu



Mosfet Reference Guide:

MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike Essential Circuits Reference Guide John Markus, Charles D. Weston, 1988 A guide to research this volume includes 925 studies of Chaucer written between 1900 and 1984 Each entry is listed once alphabetically under an appropriate topic heading or under the title of the work it treats most directly The annotations provide bibliographic information identify the primary focus of the item annotated and summarize its content See entry PR1868 These classic circuits were chosen from Markus Sourcebook of electronic circuits 1968 Electronics circuits manual 1971 and Guidebook of electronics circuits 1974 With circuit integration onto chips many older circuits have become obsolete This guide is a distillation of those circuits still in use today for which parts are still available Annotation copyrighted by Book News Inc Portland OR **MOSFET Modeling & BSIM3 User's Guide** Yuhua Cheng, Chenming Hu, 1999-09-30 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most

circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i.e. I-V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike

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Instrumentation Laboratories Stanley Wolf, Richard F. M. Smith, 1990 This book shows students how to become proficient users of electronic measuring instruments and offers a practical understanding of electrical laboratory practices

MOSFET Models for SPICE Simulation William Liu, 2001-02-21 An expert guide to understanding and making optimum use of BSIM Used by more chip designers worldwide than any other comparable model the Berkeley Short Channel IGFET Model BSIM has over the past few years established itself as the de facto standard MOSFET SPICE model for circuit simulation and CMOS technology development Yet until now there have been no independent expert guides or tutorials to supplement the various BSIM manuals currently available Written by a noted expert in the field this book fills that gap in the literature by providing a comprehensive guide to understanding and making optimal use of BSIM3 and BSIM4 Drawing upon his extensive experience designing with BSIM William Liu provides a brief history of the model discusses the various advantages of BSIM over other models and explores the reasons why BSIM3 has been adopted by the majority of circuit manufacturers He then provides engineers with the detailed practical information and guidance they need to master all of BSIM's features He Summarizes key BSIM3 components Represents the BSIM3 model with equivalent circuits for various operating conditions Provides a comprehensive glossary of modeling terminology Lists alphabetically BSIM3 parameters along with their meanings and relevant equations Explores BSIM3's flaws and provides improvement suggestions Describes all of BSIM4's improvements and new features Provides useful SPICE files which are available online at the Wiley ftp site

1993 Applications Reference Manual Analog Devices, inc, 1993 **Mosfet Modeling and Bsim3 User's Guide** Yuhua Cheng, Chenming Hu, 2014-01-15 **SPICE Version 2G.1 User's Guide** Andrei Vladimirescu, 1980 **IEEE**

Circuits & Devices ,2002 **EDN, Electrical Design News** ,1996 **Electronic Design** ,1989 **System Integration**
 Kurt Hoffmann,2004-04-16 The development of large scale integrated systems on a chip has had a dramatic effect on circuit design methodology Escalating requirements for low power high chip density circuits and systems have resulted in increasingly complex Bipolar CMOS and BICMOS technologies In order to design cost effective and reliable systems a thorough understanding of the relationship between the individual components and their impact on the performance of the integrated system is required Combining coverage of semiconductor physics digital VLSI design and analog integrated circuits in one volume this book will appeal to practising engineers circuit designers and physicists working in IC production product engineering quality management and quality testing Postgraduate students in microelectronics electrical and computer engineering will also find this book a useful reference Midwest **Electrical Engineering Reference Manual for the Electrical and Computer PE Exam** John A. Camara,2007 Electrical Engineering Reference Manual is the most comprehensive reference available for the electrical and computer engineering PE exam *Device Modeling for Analog and RF CMOS Circuit Design* Trond Ytterdal,Yuhua Cheng,Tor A. Fjeldly,2003-05-07 Bridges the gap between device modelling and analog circuit design Includes dedicated software enabling actual circuit design Covers the three significant models BSIM3 Model 9 and EKV Presents practical guidance on device development and circuit implementation The authors offer a combination of extensive academic and industrial experience Electronic Products Magazine ,1984 *EDN.* ,1985
 Proceedings of the IEEE International Symposium on Industrial Electronics ,1997 Bibliographic Guide to Computer Science ,1990

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