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Editors

More than Moore

**Creating High Value
Micro/Nanoelectronics Systems**

More Than Moore Creating High Value Micronanoelectronics Systems

Guo Qi Zhang, Alfred van Roosmalen

More Than Moore Creating High Value Micronanoelectronics Systems:

More than Moore Guo Qi Zhang,Alfred van Roosmalen,2010-01-23 In the past decades the main stream of microelectronics progress has been mainly powered by Moore's law which focuses on IC miniaturization down to nanoscale. While the microelectronics community around the world continues to invent new solutions to keep Moore's law alive there is a fast increasing need for non digital and mixed signal More than Moore MtM type technologies that are still based upon or derived from silicon technologies but do not simply scale with Moore's law. Typical examples are devices incorporating RF power high voltage passive components sensors and actuators MEMS Bio chip bio systems microfluidics solid state lighting etc. The increasing momentum of MtM is triggered by the increasing needs for high level heterogeneous system integration including non digital functions the necessity to speed up innovative product creation and to broaden the product portfolio of IC fabs and the limiting cost and time factors of advanced system on chip SoC development. It is believed that MtM will add value to society on top of and beyond advanced semiconductors technologies with fast increasing marketing potentials and that it will drive paradigm shift for technologies applications and business models *More Than Moore Heinrich Wansing,1993*

In the past decades the mainstream of microelectronics progression was mainly powered by Moore's law focusing on IC miniaturization down to nano scale. However there is a fast increasing need for More than Moore MtM products and technology that are based upon or derived from silicon technologies but do not simply scale with Moore's law. This book provides new vision strategy and guidance for the future technology and business development of micro nanoelectronics [China Semiconductor Technology International Conference 2010 \(CSTIC 2010\)](#) Han-Ming Wu,2010-03

Our mission is to provide a forum for world experts to discuss technologies address the growing needs associated with silicon technology and exchange their discoveries and solutions for current issues of high interest. We encourage collaboration open discussion and critical reviews at this conference. Furthermore we hope that this conference will also provide collaborative opportunities for those who are interested in the semiconductor industry in Asia particularly in China [3D IC Stacking Technology](#) Banqiu Wu,Ajay Kumar,Sesh Ramaswami,2011-10-14 The latest advances in three dimensional integrated circuit stacking technology. With a focus on industrial applications 3D IC Stacking Technology offers comprehensive coverage of design test and fabrication processing methods for three dimensional device integration. Each chapter in this authoritative guide is written by industry experts and details a separate fabrication step. Future industry applications and cutting edge design potential are also discussed. This is an essential resource for semiconductor engineers and portable device designers. 3D IC Stacking Technology covers High density through silicon stacking TSS technology Practical design ecosystem for heterogeneous 3D IC products Design automation and TCAD tool solutions for through silicon via TSV based 3D IC stack Process integration for TSV manufacturing High aspect ratio silicon etch for TSV Dielectric deposition for TSV Barrier and seed deposition Copper electrodeposition for TSV Chemical mechanical polishing for TSV applications Temporary and

permanent bonding Assembly and test aspects of TSV technology

Graphene and Emerging Materials for Post-CMOS

Applications Yaw Obeng,2009-05 The objectives of this symposium was to address all current and future issues related to Emerging Materials For Post CMOS Applications The symposium focused on fundamental material science characterization and applications of emerging materials designed for alternatives technologies to replace CMOS Special emphasis was placed on Beyond CMOS integration schemes technology development and on the impact of non traditional materials into nanoelectronics

[Nanotechnology and Human Health](#) Ineke Malsch,Claude Emond,2016-04-19 Addressing medium and long term expectations for human health this book reviews current scientific and technical developments in nanotechnology for biomedical agrofood and environmental applications This collection of perspectives on the ethical legal and societal implications of bionanotechnology provides unique insight into contemporary te

[Smart Systems Integration and Simulation](#) Nicola Bombieri,Massimo Poncino,Graziano Pravadelli,2016-02-17 This book presents new methods and tools for the integration and simulation of smart devices The design approach described in this book explicitly accounts for integration of Smart Systems components and subsystems as a specific constraint It includes methodologies and EDA tools to enable multi disciplinary and multi scale modeling and design simulation of multi domain systems subsystems and components at all levels of abstraction system integration and exploration for optimization of functional and non functional metrics By covering theoretical and practical aspects of smart device design this book targets people who are working and studying on hardware software modelling component integration and simulation under different positions system integrators designers developers researchers teachers students etc In particular it is a good introduction to people who have interest in managing heterogeneous components in an efficient and effective way on different domains and different abstraction levels People active in smart device development can understand both the current status of practice and future research directions

Provides a comprehensive overview of smart systems design focusing on design challenges and cutting edge solutions

Enables development of a co simulation and co design environment that accounts for the peculiarities of the basic subsystems and components to be integrated Describes development of modeling and design techniques methods and tools that enable multi domain simulation and optimization at various levels of abstraction and across different technological domains

More-than-Moore Devices and Integration for Semiconductors

Francesca Iacopi,Francis Balestra,2023 This book provides readers with a comprehensive state of the art reference for miniaturized More than Moore systems with a broad range of functionalities that can be added to 3D microsystems including flexible electronics metasurfaces and power sources The book also includes examples of applications for brain computer interfaces and event driven imaging systems Provides a comprehensive state of the art reference for miniaturized More than Moore systems Covers functionalities to add to 3D microsystems including flexible electronics metasurfaces and power sources Includes current applications such as brain computer interfaces event driven imaging and edge computing

[Integrated Nanophotonics for "More Than Moore"](#)

Zheng Wang (Ph. D.), 2017 In the past half century the International Technology Roadmap for Semiconductors ITRS has been successfully driving the development of the semiconductor industry Since the 1970s the number of components per integrated circuit has doubled every two years The trend now is widely known as Moore's Law However with the size of complementary metal oxide semiconductor CMOS transistors approaching the atomic dimension level the More Moore which implies the aggressive continuous downscaling has encountered numerous difficulties On the other hand people have realized that the value of a system does not only depend on the performance of the CMOS technology for the digital information processing but also on the functional diversification of semiconductor based devices Consequently the term More than Moore has been introduced to emphasize the trend of increasing the diversity of microelectronic chips for additional value Integrated nanophotonics could offer promising practical and profound solutions to several aspects of More than Moore such as radio frequency signal processing and biochemical sensing due to unique advantages in processing analog signals More importantly integrated nanophotonic devices could be fabricated on semiconductor based chips to build photonic integrated circuits PICs which could offer low cost high reliability and portable solutions to a variety of applications In this dissertation the design fabrication and characterization of various integrated nanophotonic devices will be presented to illustrate how integrated nanophotonics facilitates the development of More than Moore

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