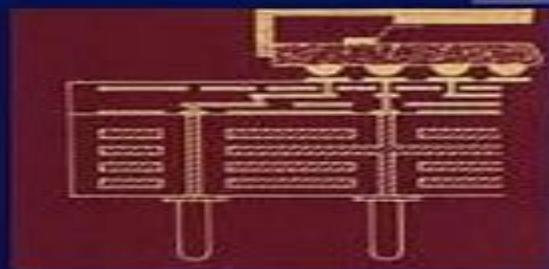


# **MICROELECTRONICS PACKAGING HANDBOOK**

## ***Subsystem Packaging***



**PART III**



**Second Edition**

Edited by

**Rao R. Tummala**  
**Eugene J. Rymaszewski**  
**Alan G. Klopfenstein**

# Microelectronics Packaging Handbook

**Ken Gilleo**

## **Microelectronics Packaging Handbook:**

Microelectronics Packaging Handbook Rao Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 1997-01-31 Provides the advances in microelectronics design methods modeling tools simulation techniques and manufacturing procedures. This book discusses packages that meet the power, cooling, protection and interconnection requirements of increasingly dense and fast microcircuitry.

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Electronics has become the largest industry surpassing agriculture, auto and heavy metal industries. It has become the industry of choice for a country to prosper already having given rise to the phenomenal prosperity of Japan, Korea, Singapore, Hong Kong and Ireland among others. At the current growth rate total worldwide semiconductor sales will reach 300B by the year 2000. The key electronic technologies responsible for the growth of the industry include semiconductors, the packaging of semiconductors for systems use in auto, telecom, computer, consumer, aerospace and medical industries, displays, magnetic and optical storage as well as software and system technologies. There has been a paradigm shift however in these technologies from mainframe and supercomputer applications at any cost to consumer applications at approximately one tenth the cost and size. Personal computers are a good example going from 500IMIP when products were first introduced in 1981 to a projected 11IMIP within 10 years. Thin, light, portable, user friendly and very low cost are therefore the attributes of tomorrow's computing and communications systems. Electronic packaging is defined as interconnection, powering, cooling and protecting semiconductor chips for reliable systems. It is a key enabling technology achieving the requirements for

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**Area Array Packaging Handbook: Manufacturing and Assembly** Ken Gilleo,2001-11-26 Covers design packaging construction assembly and application of all three approaches to Area Array Packaging Ball Grid Array BGA Chip Scale Package CSP and Flip Chip FC Details the pros and cons of each technology with varying applications Examines packaging ramifications of high density interconnects HDI

**3D Microelectronic Packaging** Yan Li,Deepak Goyal,2020-11-23 This book offers a comprehensive reference guide for graduate students and professionals in both academia and industry covering the fundamentals architecture processing details and applications of 3D microelectronic packaging It provides readers an in depth understanding of the latest research and development findings regarding this key industry trend including TSV die processing micro bumps for LMI and MMI direct bonding and advanced materials as well as quality reliability fault isolation and failure analysis for 3D microelectronic packages Images tables and didactic schematics are used to illustrate and elaborate on the concepts discussed Readers will gain a general grasp of 3D packaging quality and reliability concerns and common causes of failure and will be introduced to developing areas and remaining gaps in 3D packaging that can help inspire future research and development

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