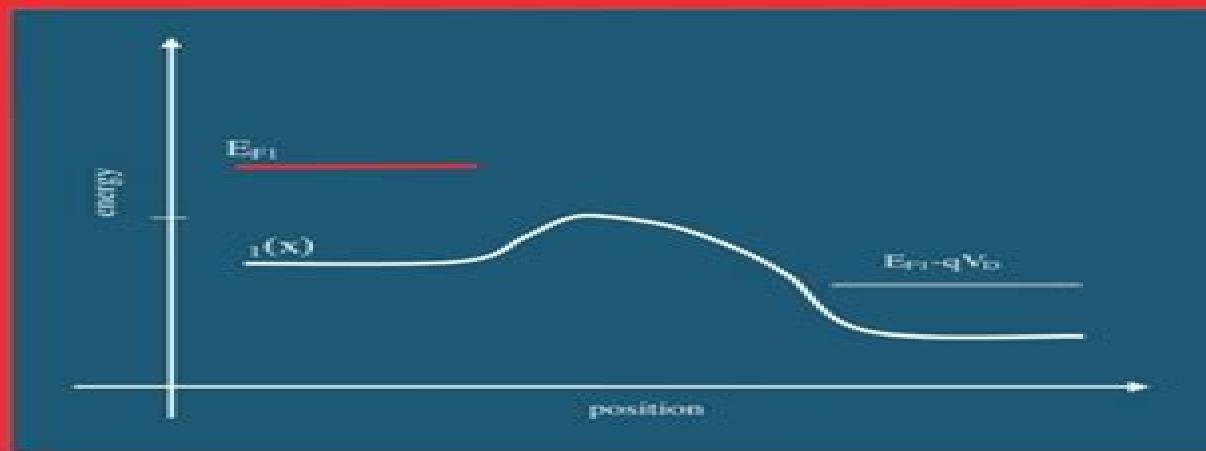


Mark Lundstrom
Jing Guo

Nanoscale Transistors

Device Physics, Modeling
and Simulation



Nanoscale Transistors Device Physics Modeling And Simulation

Yannis Tsividis, Colin McAndrew

Nanoscale Transistors Device Physics Modeling And Simulation:

Nanoscale Transistors Mark Lundstrom,Jing Guo,2006-06-18 Silicon technology continues to progress but device scaling is rapidly taking the metal oxide semiconductor field effect transistor MOSFET to its limit When MOS technology was developed in the 1960 s channel lengths were about 10 micrometers but researchers are now building transistors with channel lengths of less than 10 nanometers New kinds of transistors and other devices are also being explored Nanoscale MOSFET engineering continues however to be dominated by concepts and approaches originally developed to treat microscale devices To push MOSFETs to their limits and to explore devices that may complement or even supplant them a clear understanding of device physics at the nano molecular scale will be essential Our objective is to provide engineers and scientists with that understandin not only of nano devices but also of the considerations that ultimately determine system performance It is likely that nanoelectronics will involve much more than making smaller and different transistors but nanoscale transistors provides a specific clear context in which to address some broad issues and is therefore our focus in this monograph

Nanoscale Transistors Piero Ferri,Edward Greenberg,1989 To push MOSFETs to their scaling limits and to explore devices that may complement or even replace them at molecular scale a clear understanding of device physics at nanometer scale is necessary Nanoscale Transistors provides a description on the recent development of theory modeling and simulation of nanotransistors for electrical engineers physicists and chemists working on nanoscale devices Simple physical pictures and semi analytical models which were validated by detailed numerical simulations are provided for both evolutionary and revolutionary nanotransistors After basic concepts are reviewed the text summarizes the essentials of traditional semiconductor devices digital circuits and systems to supply a baseline against which new devices can be assessed A nontraditional view of the MOSFET using concepts that are valid at nanoscale is developed and then applied to nanotube FET as an example of how to extend the concepts to revolutionary nanotransistors This practical guide then explore the limits of devices by discussing conduction in single molecules

Nanoscale MOS Transistors David Esseni,Pierpaolo Palestri,Luca Selmi,2011-01-20 Written from an engineering standpoint this book provides the theoretical background and physical insight needed to understand new and future developments in the modeling and design of n and p MOS nanoscale transistors A wealth of applications illustrations and examples connect the methods described to all the latest issues in nanoscale MOSFET design Key areas covered include Transport in arbitrary crystal orientations and strain conditions and new channel and gate stack materials All the relevant transport regimes ranging from low field mobility to quasi ballistic transport described using a single modeling framework Predictive capabilities of device models discussed with systematic comparisons to experimental results

Operation and Modeling of the MOS Transistor Yannis Tsividis,Colin McAndrew,2011 The MOS Metal Oxide Semiconductor transistor is the most important building block of modern silicon integrated circuits This book fills an important gap in the literature by presenting a unified treatment of the operation and

modeling of the MOS transistor that is complemented with extensive intuitive discussions The MOS transistor is the dominant VLSI Very Large Scale Integration device and understanding of this device is mandatory for those people planning a career in device physics and modeling as well as in circuit design Especially important for university courses there is a logical systematic and progressive description that starts with semiconductor fundamentals and builds up to a comprehensive understanding of the basics of MOS transistors For practicing professionals there are details of nuances observed in MOS transistor behavior and various approaches to modeling these are presented Detailed derivations are given for modeling dc currents charges for large signal operation small signal operation at low frequencies and high frequencies and noise *Handbook of Research on Nanoelectronic Sensor Modeling and Applications* Ahmadi, Mohammad Taghi,Ismail, Razali,Anwar, Sohail,2016-09-20 Nanoelectronics are a diverse set of materials and devices that are so small that quantum mechanics need to be applied to their function The possibilities these devices present outweigh the difficulties associated with their development as biosensors and similar devices have the potential to vastly improve our technological reach The Handbook of Research on Nanoelectronic Sensor Modeling and Applications begins with an introduction of the fundamental concepts of nanoelectronic sensors then proceeds to outline in great detail the concepts of nanoscale device modeling and nanoquantum fundamentals Recent advances in the field such as graphene technology are discussed at length in this comprehensive handbook ideal for electrical engineers advanced engineering students researchers and academics

Fundamentals of III-V Semiconductor MOSFETs Serge Oktyabrsky,Peide Ye,2010-03-16 Fundamentals of III V Semiconductor MOSFETs presents the fundamentals and current status of research of compound semiconductor metal oxide semiconductor field effect transistors MOSFETs that are envisioned as a future replacement of silicon in digital circuits The material covered begins with a review of specific properties of III V semiconductors and available technologies making them attractive to MOSFET technology such as band engineered heterostructures effect of strain nanoscale control during epitaxial growth Due to the lack of thermodynamically stable native oxides on III V s such as SiO₂ on Si high k oxides are the natural choice of dielectrics for III V MOSFETs The key challenge of the III V MOSFET technology is a high quality thermodynamically stable gate dielectric that passivates the interface states similar to SiO₂ on Si Several chapters give a detailed description of materials science and electronic behavior of various dielectrics and related interfaces as well as physics of fabricated devices and MOSFET fabrication technologies Topics also include recent progress and understanding of various materials systems specific issues for electrical measurement of gate stacks and FETs with low and wide bandgap channels and high interface trap density possible paths of integration of different semiconductor materials on Si platform

Advanced Nanoelectronics Razali Ismail,Mohammad Taghi Ahmadi,Sohail Anwar,2018-09-03 While theories based on classical physics have been very successful in helping experimentalists design microelectronic devices new approaches based on quantum mechanics are required to accurately model nanoscale transistors and to predict their characteristics even

before they are fabricated Advanced Nanoelectronics provides research information on advanced nanoelectronics concepts with a focus on modeling and simulation. Featuring contributions by researchers actively engaged in nanoelectronics research it develops and applies analytical formulations to investigate nanoscale devices. The book begins by introducing the basic ideas related to quantum theory that are needed to better understand nanoscale structures found in nanoelectronics including graphenes, carbon nanotubes and quantum wells, dots and wires. It goes on to highlight some of the key concepts required to understand nanotransistors. These concepts are then applied to the carbon nanotube field effect transistor (CNTFET). Several chapters cover graphene, an unzipped form of CNT that is the recently discovered allotrope of carbon that has gained a tremendous amount of scientific and technological interest. The book discusses the development of the graphene nanoribbon field effect transistor (GNRFET) and its use as a possible replacement to overcome the CNT chirality challenge. It also examines silicon nanowire (SiNW) as a new candidate for achieving the downscaling of devices. The text describes the modeling and fabrication of SiNW including a new top down fabrication technique, Strained technology, which changes the properties of device materials rather than changing the device geometry, is also discussed. The book ends with a look at the technical and economic challenges that face the commercialization of nanoelectronics and what universities, industries and government can do to lower the barriers. A useful resource for professionals, researchers and scientists, this work brings together state of the art technical and scientific information on important topics in advanced nanoelectronics.

Carbon

Nanotube Electronics Ali Javey, Jing Kong, 2009-04-21 This book provides a complete overview of the field of carbon nanotube electronics. It covers materials and physical properties, synthesis and fabrication processes, devices and circuits, modeling, and finally novel applications of nanotube based electronics. The book introduces fundamental device physics and circuit concepts of 1 D electronics. At the same time it provides specific examples of the state of the art nanotube devices.

Fundamentals of Nanotransistors Mark Lundstrom, 2018 The transistor is the key enabler of modern electronics. Progress in transistor scaling has pushed channel lengths to the nanometer regime where traditional approaches to device physics are less and less suitable. These lectures describe a way of understanding MOSFETs and other transistors that is much more suitable than traditional approaches when the critical dimensions are measured in nanometers. It uses a novel bottom up approach that agrees with traditional methods when devices are large but that also works for nano devices. Surprisingly the final result looks much like the traditional textbook transistor models but the parameters in the equations have simple clear interpretations at the nanoscale. The objective is to provide readers with an understanding of the essential physics of nanoscale transistors as well as some of the practical technological considerations and fundamental limits. This book is written in a way that is broadly accessible to students with only a very basic knowledge of semiconductor physics and electronic circuits.

Women in Microelectronics Alice Cline Parker, Leda Lunardi, 2020-07-16 This book contains stories of women engineers' paths through the golden age of microelectronics, stemming from the invention of the transistor in 1947.

These stories like the biographies of Marie Curie and the National Geographic's stories of Jane Goodall's research that inspired the authors will inspire and guide readers along unconventional pathways to contributions to microelectronics that we can only begin to imagine. The book explores why and how the women writing here chose their career paths and how they navigated their careers. This topic is of interest to a vast audience from students to professionals to university advisers to industry CEOs who can imagine the advantages of a future with a diverse work force. Provides insight into women's early contributions to the field of microelectronics and celebrates the challenges they overcame. Presents compelling innovations from academia research and industry into advances applications and the future of microelectronics. Includes a fascinating look into topics such as nanotechnologies, video games, analog electronics, design automation and neuromorphic circuits.

Nanoscale Transistors Mark Lundstrom, Jing Guo, 2008-11-01 To push MOSFETs to their scaling limits and to explore devices that may complement or even replace them at molecular scale a clear understanding of device physics at nanometer scale is necessary. Nanoscale Transistors provides a description on the recent development of theory, modeling and simulation of nanotransistors for electrical engineers, physicists and chemists working on nanoscale devices. Simple physical pictures and semi-analytical models which were validated by detailed numerical simulations are provided for both evolutionary and revolutionary nanotransistors. After basic concepts are reviewed the text summarizes the essentials of traditional semiconductor devices, digital circuits and systems to supply a baseline against which new devices can be assessed. A nontraditional view of the MOSFET using concepts that are valid at nanoscale is developed and then applied to nanotube FET as an example of how to extend the concepts to revolutionary nanotransistors. This practical guide then explores the limits of devices by discussing conduction in single molecules.

Nanoscale Devices Brajesh Kumar Kaushik, 2018-11-16 The primary aim of this book is to discuss various aspects of nanoscale device design and their applications including transport mechanism modeling and circuit applications. Provides a platform for modeling and analysis of state of the art devices in nanoscale regime. Reviews issues related to optimizing the sub nanometer device performance and addresses simulation aspect and/or fabrication process of devices. Also includes design problems at the end of each chapter.

IBM Journal of Research and Development, 2006 *Nano Interconnects* Afreen Khursheed, Kavita Khare, 2021-12-23 This textbook comprehensively covers on chip interconnect dimension and application of carbon nanomaterials for modeling VLSI interconnect and buffer circuits. It provides analysis of ultra low power high speed nano interconnects based on different facets such as material modeling, circuit modeling and the adoption of repeater insertion strategies and measurement techniques. It covers important topics including on chip interconnects, interconnect modeling, electrical impedance modeling of on chip interconnects, modeling of repeater buffer and variability analysis. Pedagogical features including solved problems and unsolved exercises are interspersed throughout the text for better understanding. Aimed at senior undergraduate and graduate students in the field of electrical engineering, electronics and communications engineering for courses on Advanced

VLSI Interconnects Advanced VLSI Design VLSI Interconnects VLSI Design Automation and Techniques this book Provides comprehensive coverage of fundamental concepts related to nanotube transistors and interconnects Discusses properties and performance of practical nanotube devices and related applications Covers physical and electrical phenomena of carbon nanotubes as well as applications enabled by this nanotechnology Discusses the structure properties and characteristics of graphene based on chip interconnect Examines interconnect power and interconnect delay issues arising due to downscaling of device size **JJAP** ,2010 **Nanoscience and Nanotechnology** Mohamad Rusop,Tetsuo Soga,2009-06-22 Nano science and Nano technology are research on Material and Device Fabrication at nanometer scale that is one in a billionth meter in length Nano technology is expected to have wide and extensive usage Pharmaceutical information and communication technology and electronic and agriculture are some of the industries that will directly benefit from nanotechnology Demand for technology is at an all time high challenging and complicated Due to this scientists and technologists are working hard to produce alternative technology Nano Science and Nano Technology **Journal of Nano Research Vol. 41** Efstatios I. Meletis,2016-05-04 Special topic volume with invited peer reviewed papers only **Japanese Journal of Applied Physics** ,2008 **International Conference on Simulation of Semiconductor Processes and Devices** ,2003 **IEICE Transactions on Electronics** ,2005

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