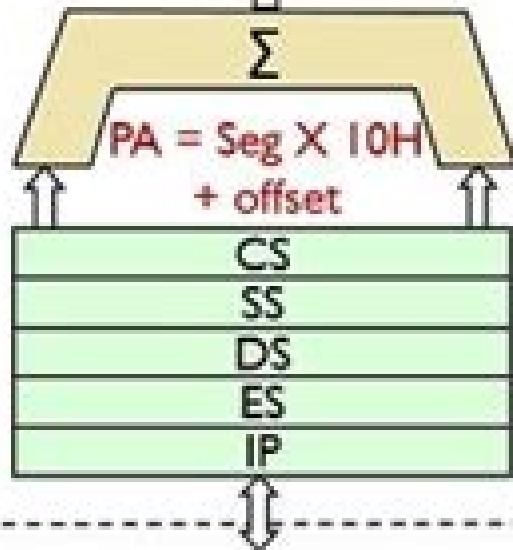


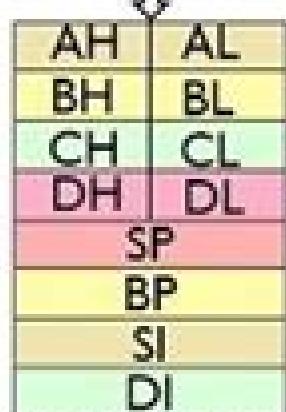
To memory and  
Input/ Output

BIU

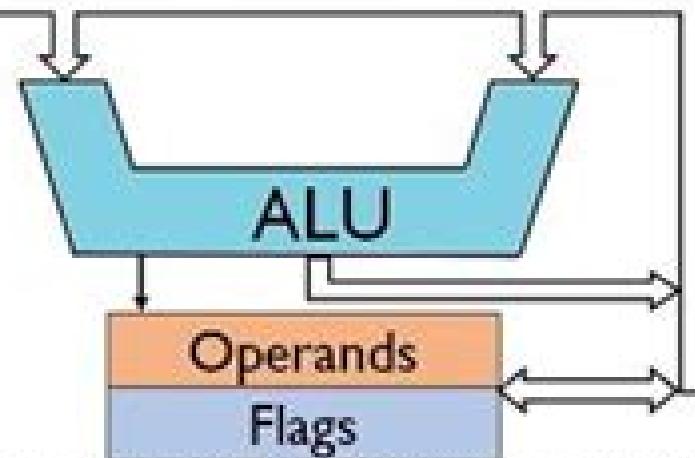


Control Unit

EU



General  
Purpose  
Registers



Block Diagram of 8086 Microprocessor

Electronics Desk

# Microprocessor 8086 Lab Manual

**Gerald Smith**

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