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**John L. Hennessy, 2012, David A.
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digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology **Optimizing HPC Applications with Intel Cluster Tools** Alexander Supalov,Andrey Semin,Christopher Dahnken,Michael Klemm,2014-10-09 Optimizing HPC Applications with Intel Cluster Tools takes the reader on a tour of the fast growing area of high performance computing and the optimization of hybrid programs These programs typically combine distributed memory and shared memory programming models and use the Message Passing Interface MPI and OpenMP for multi threading to achieve the ultimate goal of high performance at low power consumption on enterprise class workstations and compute clusters The book focuses on optimization for clusters consisting of the Intel Xeon processor but the optimization methodologies also apply to the Intel Xeon Phi coprocessor and heterogeneous clusters mixing both architectures Besides the tutorial and reference content the authors address and refute many myths and misconceptions surrounding the topic The text is augmented and enriched by descriptions of real life situations **计算机体系结构/译丛** John L. Hennessy,John A. Patterson,1999 Forthcoming Books Rose Arny,2002-04

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