

EXPERIMENT NO: 10

AIM: TO DESIGN & VERIFY OPERATION OF HALF ADDER & FULL ADDER.

APPARATUS REQUIRED: Power supply, IC's , Digital Trainer, Connecting leads .

BRIEF THEORY: We are familiar with ALU, which performs all arithmetic and logic operation but ALU doesn't perform/ process decimal no's. They process binary no's.

Half Adder: It is a logic circuit that adds two bits. It produces the O/P, sum & carry. The Boolean equation for sum & carry are

$$\text{SUM} = A + B$$

$$\text{CARRY} = A \cdot B$$

Therefore, sum produces 1 when A&B are different and carry is 1 when A&B are 1. Application of Half adder is limited.

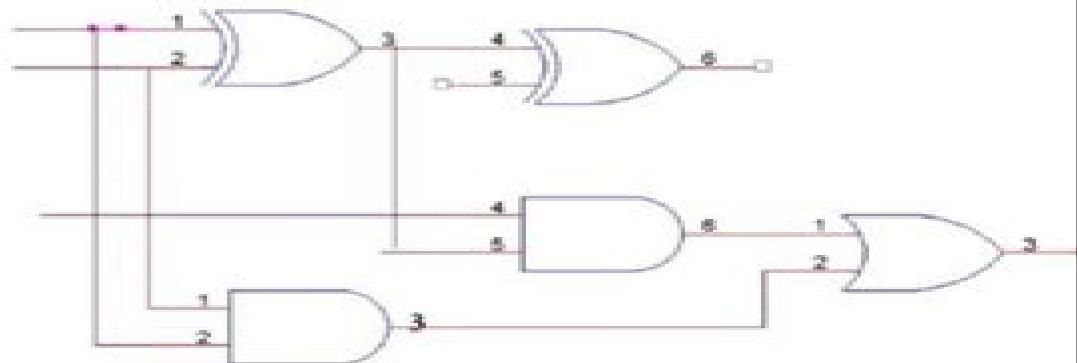
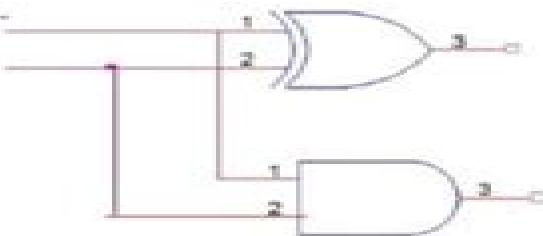
Full Adder: It is a logic circuit that can add three bits. It produces two O/P sum & carry. The Boolean Equation for sum & carry are

$$\text{SUM} = A + B + C$$

$$\text{CARRY} = A \cdot B + (A+B) \cdot C$$

Therefore, sum produces one when I/P is containing odd no's of one & carry is one when there are two or more one in I/P.

CIRCUIT DIAGRAM



HALF ADDER

FULL ADDER

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William Grant,David D. Murison

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Whitaker's Books in Print ,1998 *The Compact Scottish National Dictionary: A-M* William Grant,David D. Murison,1986

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