

MICROELECTRONICS PACKAGING HANDBOOK

Technology Update



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Microelectronics Packaging Handbook Technology Drivers Part I Pt 1

**Rao Tummala, Eugene J.
Rymaszewski, Alan G. Klopfenstein**



Microelectronics Packaging Handbook Technology Drivers Part I Pt 1:

Microelectronics Packaging Handbook R.R. Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 2012-12-06

Electronics has become the largest industry surpassing agriculture auto and heavy metal industries It has become the industry of choice for a country to prosper already having given rise to the phenomenal prosperity of Japan Korea Singapore Hong Kong and Ireland among others At the current growth rate total worldwide semiconductor sales will reach 300B by the year 2000 The key electronic technologies responsible for the growth of the industry include semiconductors the packaging of semiconductors for systems use in auto telecom computer consumer aerospace and medical industries displays magnetic and optical storage as well as software and system technologies There has been a paradigm shift however in these technologies from mainframe and supercomputer applications at any cost to consumer applications at approximately one tenth the cost and size Personal computers are a good example going from 500MIP when products were first introduced in 1981 to a projected 11MIP within 10 years Thin light portable user friendly and very low cost are therefore the attributes of tomorrow's computing and communications systems Electronic packaging is defined as interconnection powering cooling and protecting semiconductor chips for reliable systems It is a key enabling technology achieving the requirements for reducing the size and cost at the system and product level

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Semiconductor Packaging Rao R. Tummala,Eugene J. Rymaszewski,Alan G. Klopfenstein,1997 *Handbook of Microelectronics Packaging and Interconnection Technologies* F. N. Sinnadurai,1985

Electronic Materials Handbook ,1989-11-01 Volume 1

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System on Package : Miniaturization of the Entire System Rao Tummala, 2008-04-15 System on Package SOP is an emerging microelectronic technology that places an entire system on a single chip size package Where systems used to be bulky boxes housing hundreds of components SOP saves

interconnection time and heat generation by keep a full system with computing communications and consumer functions all in a single chip Written by the Georgia Tech developers of the technology this book explains the basic parameters design functions and manufacturing issues showing electronic designers how this radical new packaging technology can be used to solve pressing electronics design challenges

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