



System requirements

The computer should apply with the Teledyne PDS system requirements for correct and optimal performance of Teledyne PDS.

Operating Systems supported

- MS Windows 8 (and 8.1)
- MS Windows 10

Having a 32 bit operating system limits the amount of useable internal memory for MS Windows to 4 Gb.

Always check the latest system requirements. System requirements are available on the Teledyne PDS website. [Download system requirements](#).

The minimum requirements apply for low data consuming / processing applications such as dredge and single beam applications.

Preferred requirements apply for high data consuming / processing applications such as multibeam and side scan sonar applications.

A computer not complying with the correct system requirements results in poor performance of Teledyne PDS or even malfunctioning of the software.

Contact Teledyne RESON if in doubt.

Minimum requirements		
CPU	Modern Hyper Threading processor (we propose minimum 4 threads); or equivalent multi core processor	
Monitor	Single monitor system with minimum 1920 x 1080 resolution	
Graphic card	OpenGL 3.30 support and 2GB of memory e. g. Nvidia GeForce GTX 1050 or better	
Internal memory	8 Gb	
Hard disk	1 x 500 Gb (SSD type)	
Multi serial I/O		

Preferred requirements		
CPU	Modern Hyper Threading processor (we propose minimum 8 threads); or equivalent multi core processor	
Monitor	Dual monitor system with both minimum 1920 x 1080 resolution	
Graphic card	OpenGL 3.30 support and min. 4GB of memory e. g. Nvidia GeForce GTX 1060 / Nvidia Quadro P1000 or better	
Internal memory	16 Gb	
Hard disk	1 Tb or more (SSD type)	
Multi serial I/O		
Network	Gigabit Ethernet (1000 Mbps)	

Installing Teledyne PDS

Do not insert the PDS dongle before first time installation of PDS.

Latest PDS version can be downloaded from the PDS website <http://www.teledyne-marine.com/pds/>

Follow the installation wizard to install Teledyne PDS:

1. Open from the Teledyne PDS distribution the **Setup.exe** application.



2. The installation wizard opens.

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Pds Installation Guide

SJ Ball

Pds Installation Guide:

The AT&T Documentation Guide, 1993-06 Catalog of the most often requested AT T documents **Official (ISC)2® Guide to the CISSP®-ISSEP® CBK®** Susan Hansche, 2005-09-29 The Official ISC 2 Guide to the CISSP ISSEP CBK provides an inclusive analysis of all of the topics covered on the newly created CISSP ISSEP Common Body of Knowledge The first fully comprehensive guide to the CISSP ISSEP CBK this book promotes understanding of the four ISSEP domains Information Systems Security Engineering ISSE Certifica

The Complete Guide to CICS Transaction Gateway

Volume 1 Configuration and Administration Rufus Credle,Sue Bayliss,Leigh Compton,Robert Jones,Manuela Mandelli,Richard Mercadante,IBM Redbooks,2014-08-08 In this IBM Redbooks publication you will gain an appreciation of the IBM CICS Transaction Gateway CICS TG product suite based on key criteria such as capabilities scalability platform CICS server support application language support and licensing model Matching the requirements to available infrastructure and hardware choices requires an appreciation of the choices available In this book you will gain an understanding of those choices and will be capable of choosing the appropriate CICS connection protocol APIs for the applications and security options You will understand the services available to the application developer when using a chosen protocol You will then learn about how to implement CICS TG solutions taking advantage of the latest capabilities such as IPIC connectivity high availability and Dynamic Server Selection Specific scenarios illustrate the usage of CICS TG for IBM z OS and CICS TG for Multiplatforms with CICS Transaction Server for z OS and IBM WebSphere Application Server including connections in CICS

configuring simple end to end connectivity all platforms with verification for remote and local mode applications and adding security XA support and high availability **ISV IBM zPDT Guide and Reference** Bill Ogden,IBM Redbooks,2022-12-13 This IBM Redbooks publication provides both introductory information and technical details for ISV IBM Z Program Development Tool IBM zPDT which produces a small IBM zSystems environment that is suitable for application development ISV zPDT is a personal computer PC Linux application When ISV zPDT is installed on Linux normal IBM zSystems operating systems such as IBM z OS may be run on it ISV zPDT provides the basic IBM zSystems architecture and provides emulated IBM 3390 disk drives 3270 interfaces Open Systems Adapter OSA interfaces and other items The systems that are described in this publication are complex with elements of Linux for the underlying PC machine IBM z Architecture for the core zPDT elements IBM zSystems I O functions for emulated I O devices z OS the most common IBM zSystems operating system and various applications and subsystems under z OS We assume that the reader is familiar with general concepts and terminology of IBM zSystems hardware and software elements and with basic PC Linux characteristics This publication provides the primary documentation for ISV zPDT and corresponds to zPDT V1 R11 commonly known as GA11 **AD Products and Services Guide** United States. Department of Energy. Office of Administration and Human Resource Management,1992 **Personal Computer Buyers Guide** Dennis J. Grimes,Brian W. Kelly,1983 **Online Micro-software**

Guide & Directory ,1983 *Energy Efficiency in Motor Systems* Peter Radgen,Paolo Bertoldi,2024-11-06 This book contains selected peer reviewed papers presented at the 12th International Conference on Energy Efficiency in Motor Systems EEMODS 22 held in Stuttgart Germany from May 3 5 2022 As with previous conferences in this series EEMODS 22 provided a scientific forum to discuss and debate the latest developments and impacts of electrical motor systems on energy and the environment energy efficiency policies and programs adopted and planned standards including ISO 50 001 and the technical and commercial advances made in the dissemination and penetration of energy efficient motor systems Topics covered include emerging motor technologies research and innovation in electric motors power electronics and drives pump systems market surveillance and enforcement mechanisms national energy efficiency standards including case studies plus much more The conference is international by nature and aims to attract high quality and innovative contributions from all corners of the globe while the papers facilitate the development of new technologies policies and strategies to increase energy efficiency *SAX Observers' Handbook* L. Piro,R. C. Butler,1995 *Observers' Guide* Anglo-Australian Observatory,1991

Guide to Real Estate & Mortgage Banking Software ,1990 **A Practical Guide to EDP Auditing** James Hannan,1982

Year 2000 Mainframe Survival Guide Sunny Easwaran,Mary Easwaran,1999 The practical guide for every circuit designer creating FPGA designs with Verilog Walk through design step by step from coding through silicon Partitioning synthesis simulation test benches combinatorial and sequential designs and more Real World FPGA Design with Verilog guides you through every key challenge associated with designing FPGAs and ASICs using Verilog one of the world s leading hardware design languages You ll find irreverent yet rigorous coverage of what it really takes to translate HDL code into hardware and how to avoid the pitfalls that can occur along the way Ken Coffman presents no frills real world design techniques that can improve the stability and reliability of virtually any design Start by walking a typical Verilog design all the way through to silicon then review basic Verilog syntax design simulation and testing advanced simulation and more Coverage includes Essential digital design strategies recognizing the underlying analog building blocks used to create digital primitives implementing logic with LUTs clocking strategies logic minimization and more Key engineering tradeoffs including operating speed vs latency Combinatorial and sequential designs Verilog test fixtures compiler directives and automated testing A detailed comparison of alternative architectures and software including a never before published FPGA technology selection checklist Real World FPGA Design with Verilog introduces libraries and reusable modules points out opportunities to reuse your own code and helps you decide when to purchase existing IP designs instead of building from scratch Essential rules for designing with ASIC conversion in mind are presented If you re involved with digital hardware design with Verilog Ken Coffman is a welcome voice of experience showing you the shortcuts helping you over the rough spots and helping you achieve competence faster than you ever expected *CD-ROM Professional's CD-recordable Handbook* Dana Parker,Bob Starrett,1996 CD ROM contains searchable full text of the book working demos of powerful CD ROM software programs and

utilities for CD ROM users **Development Systems Handbook** Intel Corporation,1983 **Instrumentation Technology** ,1974 **Regional Industrial Buying Guide** ,2000 **Guide to the Macintosh Family Hardware** ,1990
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